

产 品 规 格 书

Product Specification

Model Name (产品名称)		TFT-LCD Module	
Part Number (产品型号)		JT152FHI40R5L01	
Item (项目)	Prepared (拟制)	Checked (审核)	Approved (批准)
Signature (签名)	赖依	林勇	
Date (日期)	2023-11-22	2023-11-22	
Note (备注)			
Customer: (客户)		Signature (签名)	
		Date (日期)	

Revision Record

Version	Issued Date	Page	Description of changes	New Description
01	2023-11-22	\	Initial release	\

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1. General Description

1.1 Product Features

- FHD Resolution (1920 * 960)
- Very High Contrast Ratio: 3000:1
- Ultra Wide Viewing Angle: 178°(H)/178°(V) (CR≥10)
- DE (Data Enable) Mode
- LVDS (Low Voltage Differential Signaling) Interface

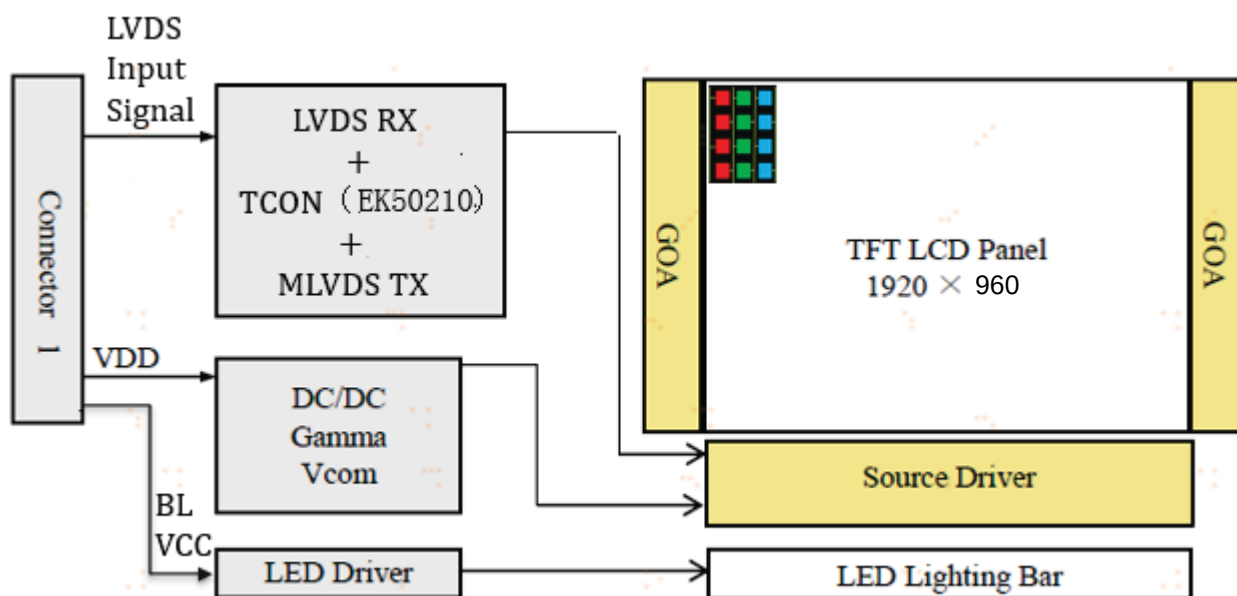


Fig. 1.1 Driver Architecture

1.2 Overview

This is a color active matrix liquid crystal display. The matrix employs a-Si thin film transistor as the active element. This module is a diagonal 15.2" color active matrix LCD open cell with 2ch-LVDS interface, which open cell is a transmissive type display operating in the normally black mode. It supports 1920 * 960 FHD resolution and can display up to 16.7M colors (8bit). Each pixel is divided into Red, Green and Blue sub-pixels which are arranged in vertical stripe.

This module dedicates for Public information display products and provides excellent performance which includes high contrast ratio, high color saturation and high color depth. CSOT open cell comply with RoHS for identification.

1.3 GENERAL SPECIFICATIONS

Items	Specification	Unit	Note
Active Screen Size	15.2	inch	
Number of Pixels	1920 X 960	Pixel	
Display Area	344.16(H) x 172(V)	mm	
Bezel Opening	347 (H) x175 (V)	mm	
Outline Dimension	368(H) x 196(V) x 12.7(D)	mm	
Luminance, White	500 (Center 1 point, Typ.)	cd/m ²	
Power Supply Input Voltage	12 (Typ)	V	Backlight
Power Supply Voltage	12(Typ)	V	OC
Power Consumption	Total = 10.25 (Typ.)	W	LCD Open Cell = 2.45W(Typ.) Backlight = 7.8W(Typ.)
Contrast Ratio	3000 : 1	--	
Response time	30 (Typ.)(G to G)	ms	
Color temperature	10681	K	
Weight	1.25(Typ)	Kg	
Operating Temperature	0 ~ 50	°C	
Storage Temperature	-20 ~60	°C	

2. Absolute Maximum Ratings

2.1 Absolute Maximum Ratings ($T_A = 25 \pm 2 \text{ }^\circ\text{C}$)

The followings are maximum values which, if exceeded, may cause damage to the unit.

Item	Symbol	Value		Unit
		Min.	Max.	
Power Supply Voltage	V_{CC}	-0.3	13.2	V
Input Signal Voltage	V_{IN}	-0.3	3.6	V

2.2 Environment Requirement (Based on CSOT's BLU)

(1) Temperature and relative humidity range are shown as below.

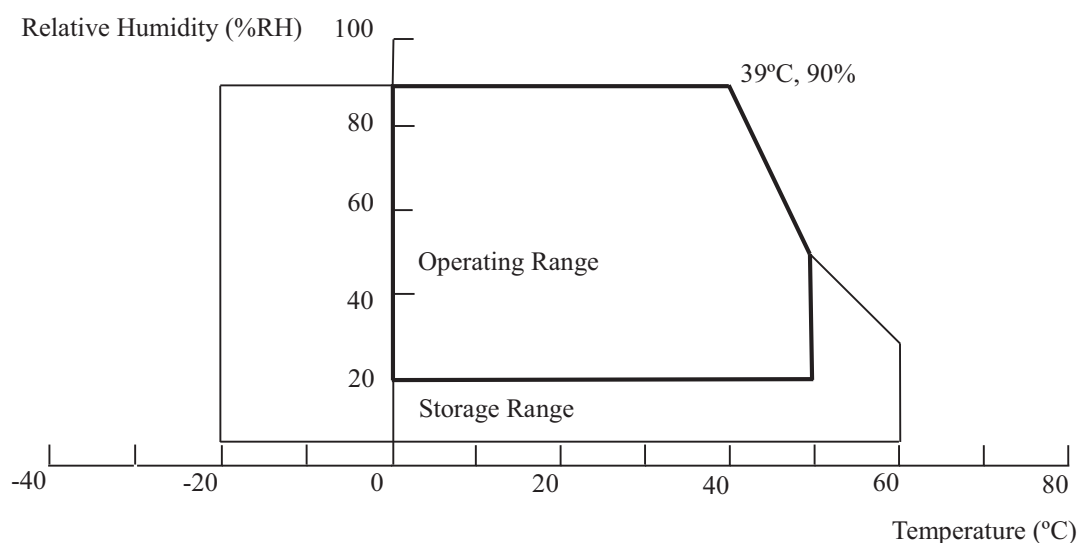


Fig. 2.1 Operating and storage environment

- (a) 90%RH maximum ($T_A \leq 39 \text{ }^\circ\text{C}$).
- (b) Wet-bulb temperature should be 39°C maximum ($T_A > 39 \text{ }^\circ\text{C}$).
- (c) No condensation.

(2) The storage temperature is between - 20 °C to 60 °C, and the operating ambient temperature is between 0 °C to 50 °C

The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 65°C with LCD module in a temperature controlled chamber alone. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 65°C. The range of operating temperature may degrade in case of improper thermal management in the end product design.

(3) The rating of environment is based on LCD module. Leave LCD cell alone, this environment condition can't be guaranteed.

Except LCD cell, the customer has to consider the ability of other parts of LCD module and LCD module process.

2.3 Absolute Ratings of Environment

When storing open cell as spares for a long time, please follow the precaution instructions:

- (1) Do not store the open cell in high temperature and high humidity for a long time. It is highly recommended to store the open cell with temperature from 20°C to 30°C in normal humidity ($50 \pm 10\%RH$) with shipping package.
- (2) The open cell should be kept within one month shelf life.

3. Electrical Specifications

3.1 Open Cell Power Consumption ($T_A = 25 \pm 2 \text{ }^{\circ}\text{C}$)

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V _{CC}	10.8	12	13.2	V	(1)
Rush Current		I _{rush}	-	-	3	A	(2)
Power Supply Current	White Pattern	I _{CC}	-	0.209	0.209	A	(3) 60Hz
	Horizontal Stripe		-	0.201	0.311	A	
	Black Pattern		-	0.2	0.20	A	
	Mosaic Pattern		-	0.204	0.204	A	
Power Consumption (Mosaic Pattern)		P _{OC}	--	2.448	3.168	Watt	60Hz

Note:

(1) The ripple voltage should be controlled less than 10% of V_{CC}.

(2) Measurement condition: V_{CC} rising time = 470μs.

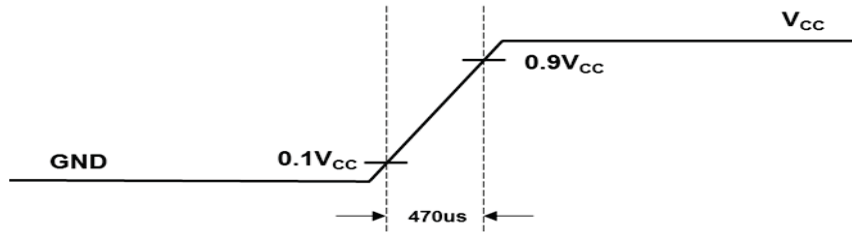


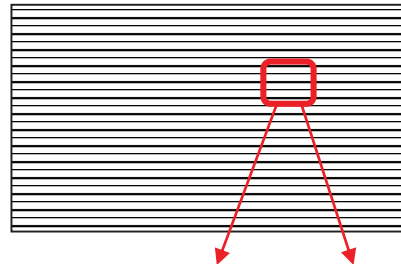
Fig. 3.1 VCC rising time condition

(3) Measurement condition: V_{CC} = 12 V, $T_A = 25 \pm 2 \text{ }^{\circ}\text{C}$. The test patterns are shown as below.

A. White Pattern



B. Horizontal Stripe Pattern



C. Black Pattern

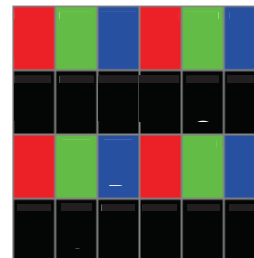


Fig. 3.2 Test patterns

3.2 LVDS Characteristics

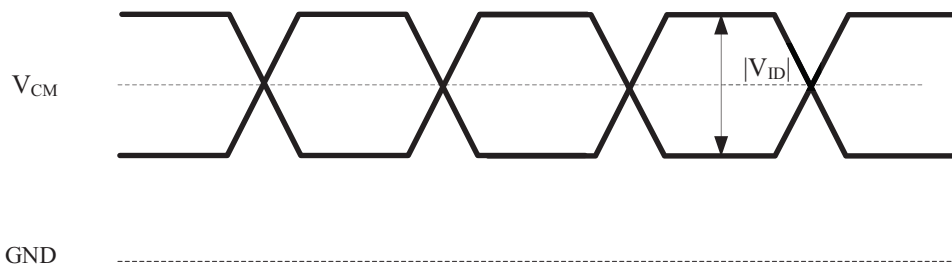
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
LVDS Interface	Differential Input High Threshold Voltage	VTH	-	-	+100	mV	(1)
	Differential Input Low Threshold Voltage	VTL	-100	-	-	mV	
	Common Input Voltage	VCM	1.0	1.2	1.4	V	
	Differential Input Voltage	VID	100	-	600	mV	
	Terminating Resistor	RT	87.5	100	112.5	ohm	
CMOS Interface	Input High Threshold Voltage	VIH	2.7	-	3.3	V	
	Input Low Threshold Voltage	VIL	0	-	0.7	V	

Note:

(1) The product should be always operated within above ranges.

(2) The LVDS input signal has been defined as follows:

Single end Signals



Differential Signal

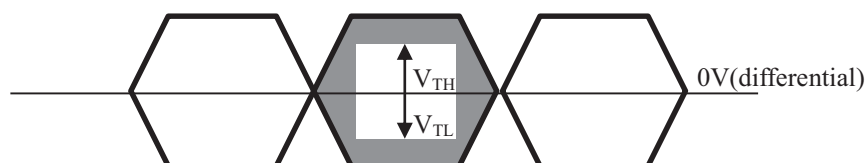


Fig. 3.3 LVDS input signal

3.3 Temperature Specifications

Parameter	Symbol	Spec			Unit	Note
		Min.	Typ.	Max.		
Source driver	T _{DRIVER}	-	-	115	°C	(1)
PMIC	T _{PMIC}	-	-	100	°C	(1)
TCON	T _{TCON}	-	-	105	°C	(1)

Note:

- (1) Any point on the IC surface must be less than Max. specification under any condition ,If the surface temperature is out of the specification, thermal solutions should be applied to avoid to be damaged.

3.4 Driver IC ESD Specification

The Electro-Static Discharge tolerance of Source COF IC is $\pm 2\text{KV}$ tested by ESD Gun. Especially if the LCD module is designed with the Plastic Bezel, we suggest ESD protection solutions should be applied to avoid be damaged, as shown in Fig.3.4.

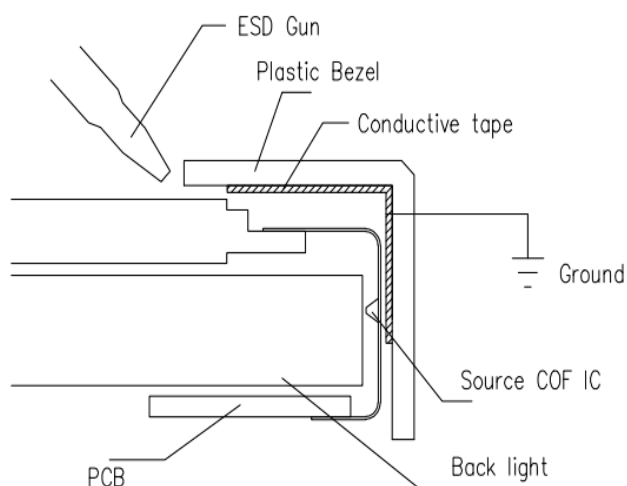


Fig. 3.4 Source COF IC ESD protection

4. Input Terminal Pin Assignment

4.1 Interface Pin Assignment

CN1: 300E40-0010RA-G3-D (CT) or equivalent (see Note (1))

Pin No.	Symbol	I/O	Description	Note
1	RxO0-	I	Negative LVDS differential data input (Odd data)	
2	RxO0+	I	Positive LVDS differential data input (Odd data)	
3	RxO1-	I	Negative LVDS differential data input (Odd data)	
4	RxO1+	I	Positive LVDS differential data input (Odd data)	
5	RxO2-	I	Negative LVDS differential data input (Odd data)	
6	RxO2+	I	Positive LVDS differential data input (Odd data)	
7	GND	P	Ground	
8	RxOCLK-	I	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	I	Positive LVDS differential clock input (Odd clock)	
10	GND	P	Ground	
11	RxO3-	I	Negative LVDS differential data input (Odd data)	
12	RxO3+	I	Positive LVDS differential data input (Odd data)	
13	GND	P	Ground	
14	RxE0-	I	Negative LVDS differential data input (Even data)	
15	RxE0+	I	Positive LVDS differential data input (Even data)	
16	RxE1-	I	Negative LVDS differential data input (Even data)	
17	RxE1+	I	Positive LVDS differential data input (Even data)	
18	RxE2-	I	Negative LVDS differential data input (Even data)	
19	RxE2+	I	Positive LVDS differential data input (Even data)	
20	GND	P	Ground	
21	RxECLK-	I	Negative LVDS differential clock input (Even clock)	
22	RxECLK+	I	Positive LVDS differential clock input (Even clock)	
23	GND	P	Ground	
24	RxE3-	I	Negative LVDS differential data input (Even data)	
25	RxE3+	I	Positive LVDS differential data input (Even data)	
26	GND	P	Ground	
27	LCD_VCC	P	LCD VCC(12V)	
28	LCD_VCC	P	LCD VCC(12V)	
29	BIST	I	LCD self-test (Normal mode: NC or pull L; BIST mode: pull H)	
30	BL_ENABLE	I	Backlight on/off	(+3.3V Input)
31	BL_PWM_DIM	I	System PWM	
32	BL_POWER	P	LED Power Supply Input Voltage(12V)	

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33	BL_POWER	P	LED Power Supply Input Voltage(12V)	
34	BL_POWER	P	LED Power Supply Input Voltage(12V)	
35	BL_POWER	P	LED Power Supply Input Voltage(12V)	
36	GND	P	Ground	
37	GND	P	Ground	
38	GND	P	Ground	
39	ID1	O	Reserved PIN, Default 'H' , Recommend NC	(2)
40	ID2	O	Reserved PIN, Default 'L' , Recommend NC	(2)

I:input signal. O:output signal. P:Power.

Note:

(1)The direction of pin assignment is shown as below:

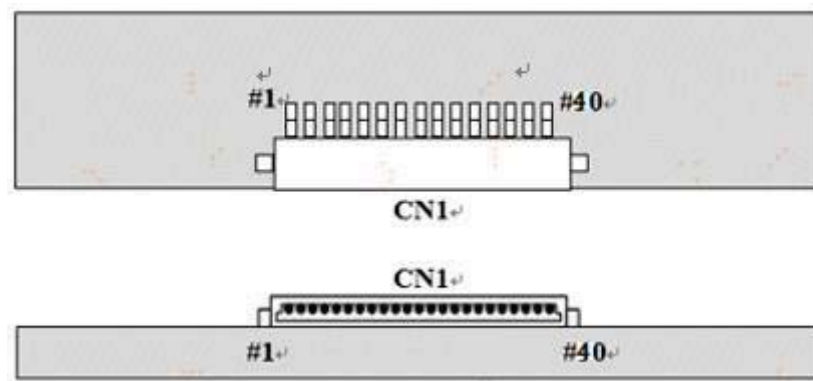


Fig. 4.1 LVDS connector direction sketch map

(2)Please let it open if it do not used.

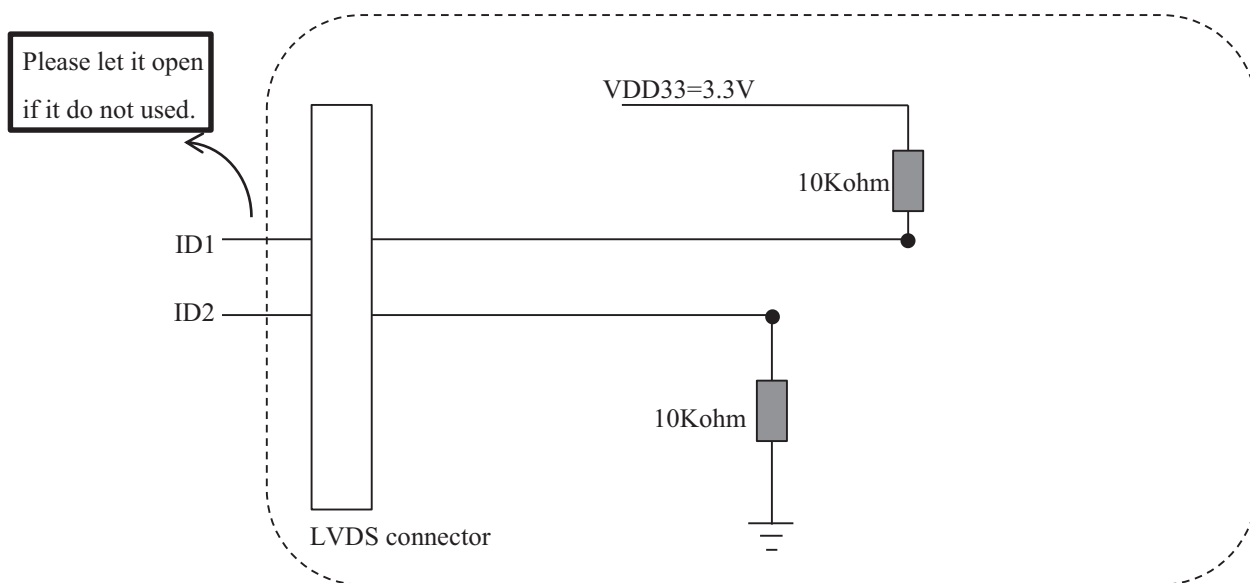


Fig. 4.2 ID1/ID2 set

4.2 Block Diagram of Interface

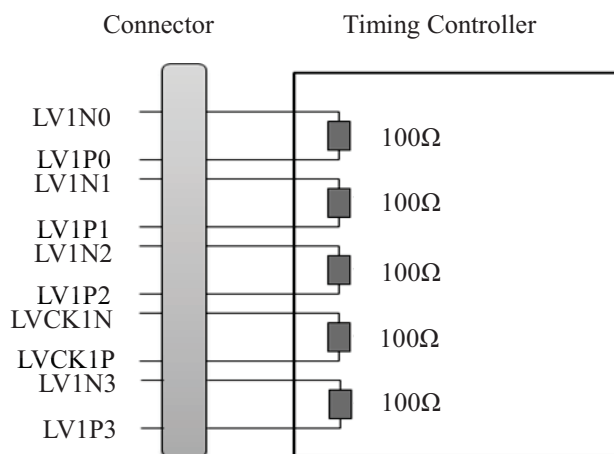


Fig. 4.4 Block diagram of interface

Attention:

- (1) This open cell uses a 100 ohms (Ω) resistor between positive and negative lines of each receiver input.
- (2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line respectively

4.3 LVDS Interface

4.3.1 VESA Format

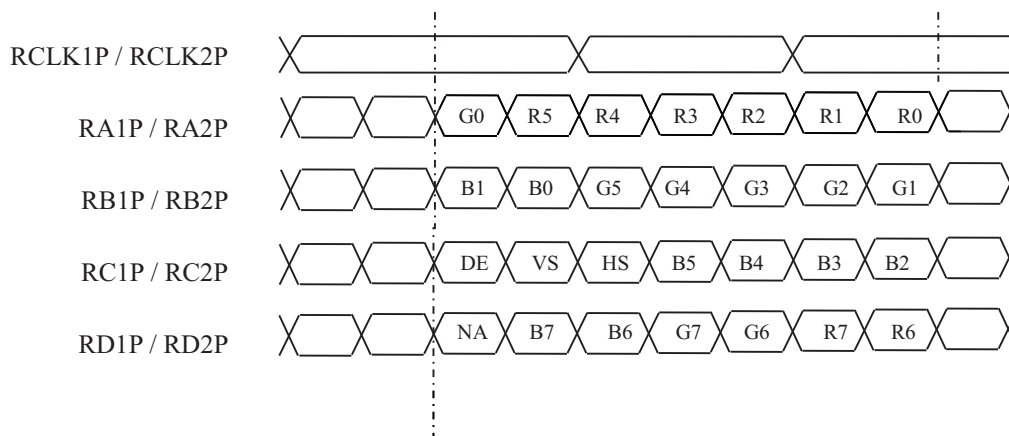
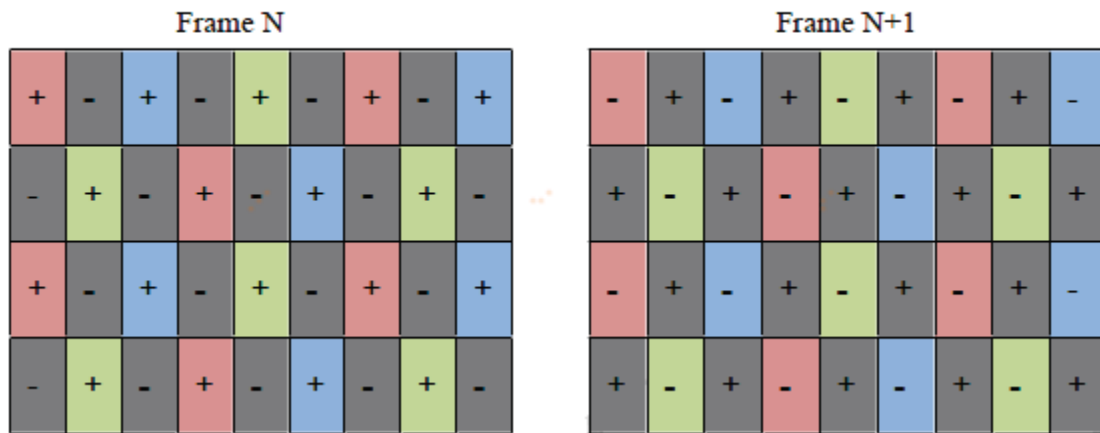


Fig. 4.5 VESA format

4.4 Flicker Pattern

Flicker should be adjusted by the Dot on/off pattern, where are displayed alternately at vertical line. (Dot inversion)

Dot inversion pattern



5. Interface Timing

5.1 Timing Table (DE Only Mode)

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Receiver Clock	Frequency	Felkin (=1/TClk)	59.4	74.25	77.34	MHz	(1) (2)
	Input cycle to cycle jitter	Trcl	-	-	200	ps	(3)
	Spread spectrum modulation range	Felkin_mod	Felkin-2%	-	Felkin+2%	MHz	(4)
	Spread spectrum modulation frequency	FSSM	60	-	200	KHz	
LVDS Receiver Data	Receiver Skew Margin	TRSM	-400	-	400	ps	(5)
Vertical Active Display Term	Frame Rate	F	48	60	62.5	Hz	
	Total	TV	1092	1125	1380	TH	TV = TVD +TVB
	Display	TVD	1080				
	Blank	TVB	12	45	300	TH	
Horizontal Active Display Term	Total	TH	1046	1100	1174	TCLK	TH = THD + THB
	Display	THD	960				960=1920/2port
	Blank	THB	86	140	214	TCLK	

Note:

- (1) The TFT LCD open cell is operated in DE only mode, H sync and V sync input signal have no effect on normal operation.
(2) Please make sure the range of pixel clock follows the following equations:

$$Felkin(max) \geq Fmax \times Tv \times Th \quad Fmin \times Tv \times Th \geq Felkin(min) \quad 74.25MHz=148.5/2port \text{ LVDS}$$

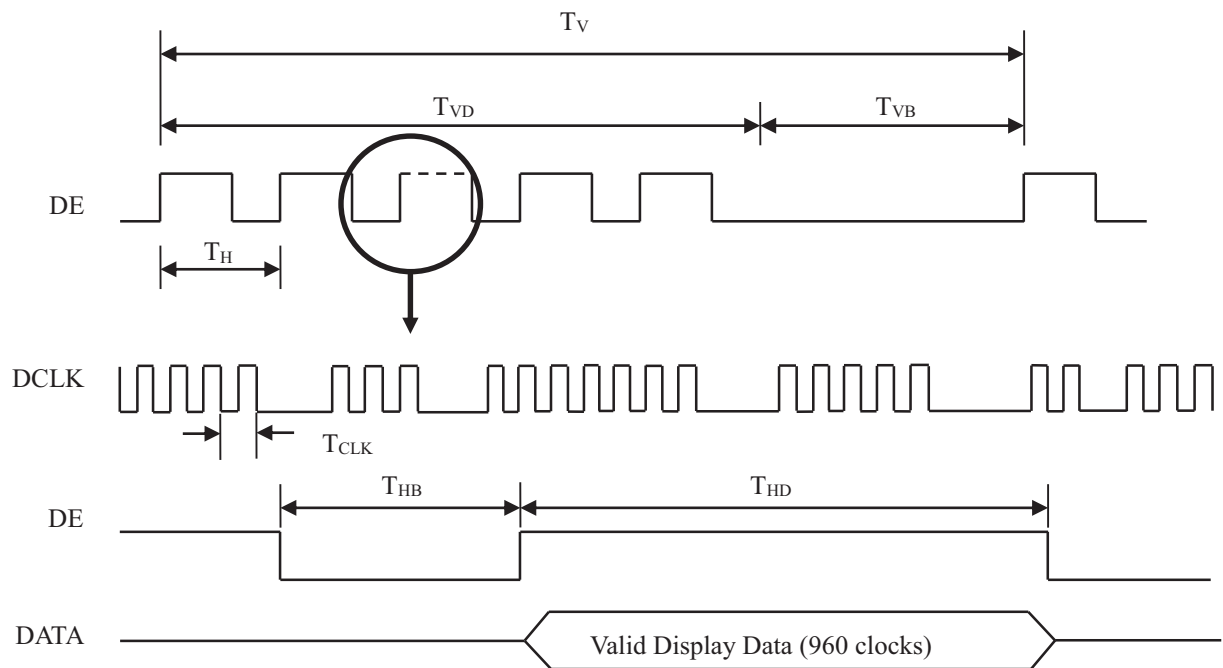


Fig. 5.1 Interface signal timing diagram

(3)The input clock cycle-to-cycle is defined as below figures.

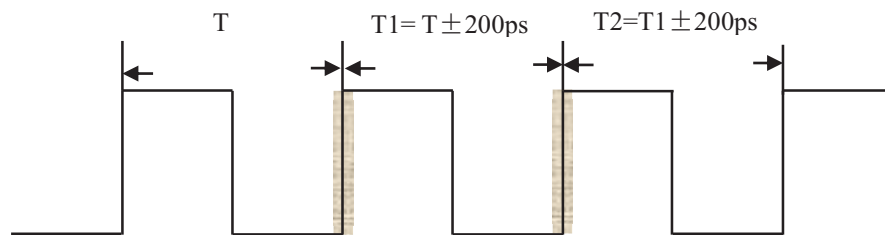


Fig. 5.2 Jitter

(4) The SSM (Spread Spectrum Modulation) is defined as the following figure.

The LVDS SSM 's suggestion is off by default, SOC board must test all validation if SOC board open the LVDS SSM.

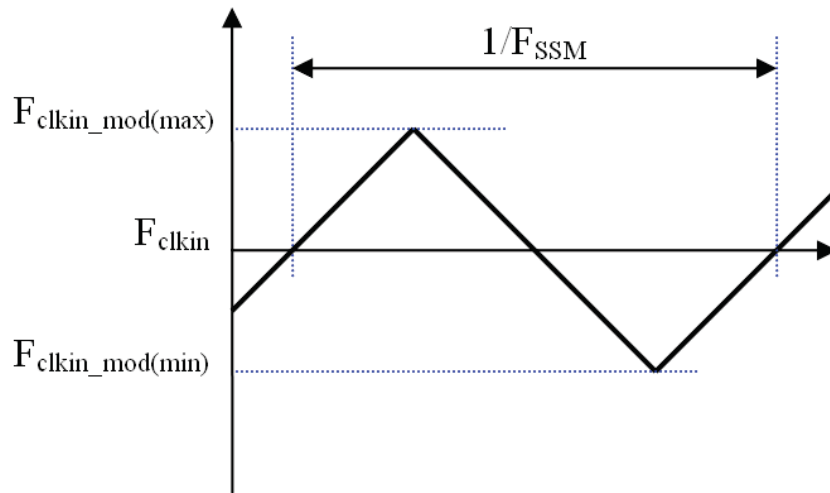


Fig. 5.3 SSM

(5) The LVDS timing diagram and setup/hold time is defined and showed as the following figure.

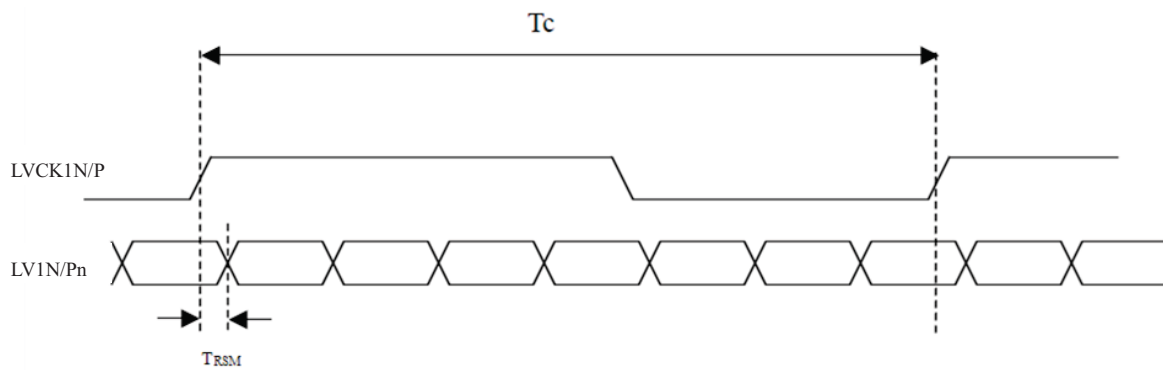


Fig.5.4 LVDS receive interface timing diagram

5.2 Power On/Off Sequence

To prevent a latch-up or DC operation of the Open cell, the power on/off sequence should be as the diagram below.

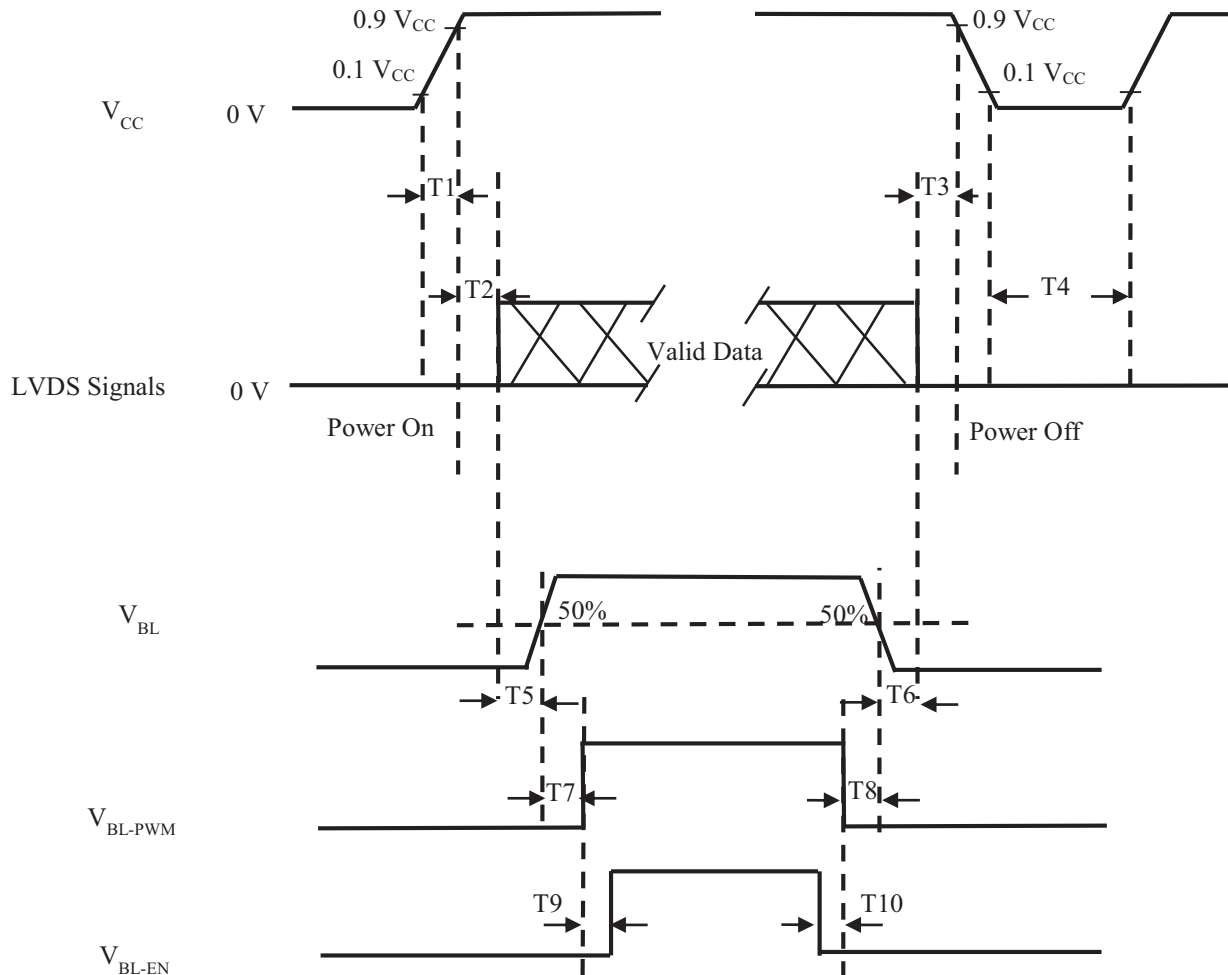


Fig.5.5 Power on/off sequence

Parameter	Values			Unit	Note
	Min.	Typ.	Max.		
T1	0.5	-	10.0	ms	
T2	0.0	50	200	ms	
T3	0.0	50	200	ms	
T4	1000.0	-	-	ms	
T5	500.0	-	-	ms	
T6	100.0	-	-	ms	
T7	0	-	-	ms	
T8	0	-	-	ms	
T9	0	-	-	ms	
T10	0	-	-	ms	

Attention:

- (1) The supply voltage of the external system for the open cell input should follow the definition of VCC.
- (2) When the customer's backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.
- (3) In case that VCC is in off level, please keep the level of input signals on the low or high impedance. If $T2 < 0$, that may cause electrical overstress.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

6 Backlight Unit

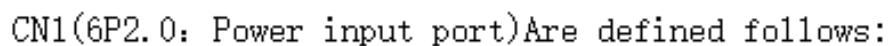
Parameter	Symbol	Values			Unit	notes
		Min	Typ	Max		
LED Driver :						
Power Supply Input Voltage	VBL	TBD	12.3	12.8	Vdc	1
Power Supply Input Current	IBL	-	0.64	0.68	A	1
Power Supply Input Current (In-Rush)	In-rush	-	-	(TBD)	A	VBL = 12.0V ExtV _{BR-B} = 100% 3
Power Consumption	PBL	-	7.8	8.7	W	1
LED :						
Life Time			50,000		Hrs	2

notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at $25 \pm 2^\circ\text{C}$. The specified current and power consumption are under the typical supply Input voltage 24V and VBR (ExtV_{BR-B} : 100%), it is total power consumption.
2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtV_{BR-B} : 100%) on condition of continuous operating in LCM state at $25 \pm 2^\circ\text{C}$.
3. The duration of rush current is about 200ms. This duration is applied to LED on time.
4. Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied. ExtV_{BR-B} signal have to input available duty range and sequence.
5. After Driver ON signal is applied, ExtV_{BR-B} should be sustained from 30% to 100% more than 500ms. After that, ExtV_{BR-B} 30% and 100% is possible

(1) Dimension of P.C.B. / PCB 机械尺寸：长 95mm、宽 42mm、高 10mm

图 1-1 PCB 恒流板机械尺寸



1-2	power input VCC
5-6	power input GND
3	ENA on/off contro(+3.3V)
4	PWM/DIM dimming control

7. Optical Characteristics

7.1 Measurement Conditions

The table below is the test condition of optical measurement.

Item	Symbol	Value	Unit
Ambient Temperature	T_A	25 ± 2	$^{\circ}\text{C}$
Ambient Humidity	H_A	50 ± 10	%RH
Driving Signal	Refer to the typical value in Chapter 3: Electrical Specification		
Vertical Refresh Rate	F_R	60	Hz

To avoid abrupt temperature change during optical measurement, it's suggested to warm up the LCD module more than 20 minutes after lighting the backlight and in the windless environment.

To measure the LCD Module, it is suggested to set up the standard measurement system as Fig. 7.1. The measuring area S should contain at least 500 pixels of the LCD cell as illustrated in Fig. 7.2 (A means the area allocated to one pixel). In this model, for example, the minimum measuring distance Z is 370mm when θ is 2 degree. Hence, 500mm is the typical measuring distance. This measuring condition is referred to 301-2H of VESA FPD 2.0 about viewing distance, angle, and angular field of view definition.

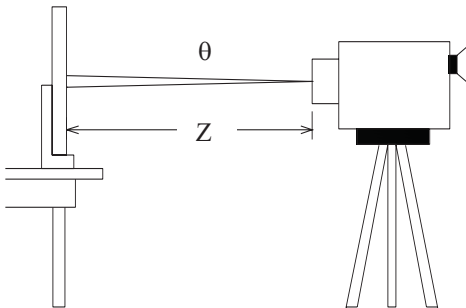


Fig. 7.1 The standard set-up system of measurement

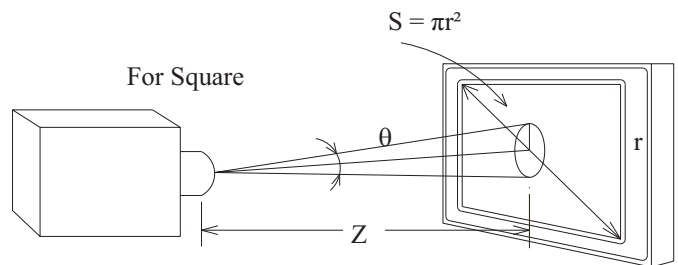


Fig. 7.2 The area S contains at least 500 pixels to be measured

$$N = \frac{S}{A} \geq 500 \text{ pixels}$$

N means the actual number of the pixels in the area S .

7.2 Optical Specifications

The table below of optical characteristics is measured by MINOLTA CS2000, ELDIM OPTI Scope-SA and ELDIM EZ contrast in dark room.

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Remark
Viewing Angle	Horizontal	Θ_3	CR > 10	-	89	-	Deg.	-
		Θ_9		-	89	-	Deg.	
	Vertical	Θ_{12}		-	89	-	Deg.	
		Θ_6		-	89	-	Deg.	
Contrast ratio		CR	$\Theta = 0^\circ$ (Center) Normal Viewing Angle	-	3000:1	-		-
Reproduction of color	White	W_x		TYP. - 0.03	0.2740	TYP. + 0.03		Based on BOE QE Backlight
		W_y			0.2899			
	Red	R_x			0.6534			
		R_y			0.3258			
	Green	G_x			0.3130			
		G_y			0.6072			
	Blue	B_x			0.1435			
		B_y			0.0593			
Response Time	G to G	T_g		-	30		ms	-
Luminance White					400	500		cd/m ²

Note:

- (1) Definition of static contrast ratio (CR):

It's necessary to switch off all the dynamic and dimming function when measuring the static contrast ratio.

$$\text{Static Contrast Ratio (CR)} = \frac{\text{CR} - W}{\text{CR} - D}$$

CR-W is the luminance measured by LMD (light-measuring device) at the center point of the LCD module with full-screen displaying white. The standard setup of measurement is illustrated in Fig. 7.3; CR-D is the luminance measured by LMD at the center point of the LCD module with full-screen displaying black. The LMD in this item is CS2000.

- (2) The LMD in the item could be a spectrometer such as (KONICA MINOLTA) CS2000, CS1000 (TOPCON), SR-UL2 or the same level spectrometer. Other display color analyzer (KONICA MINOLTA) CA210, CA310 or (TOPCON) BM-7 could be involved after being calibrated with a spectrometer on each stage of a product.

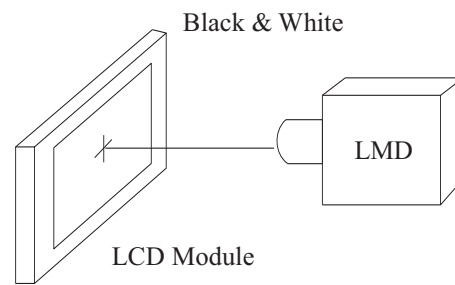


Fig. 6.3 The standard setup of CR measurement

(3) The electro-optical response time measurements shall be made as Figure 7.4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_{on} , and 90% to 10% is T_{off} .

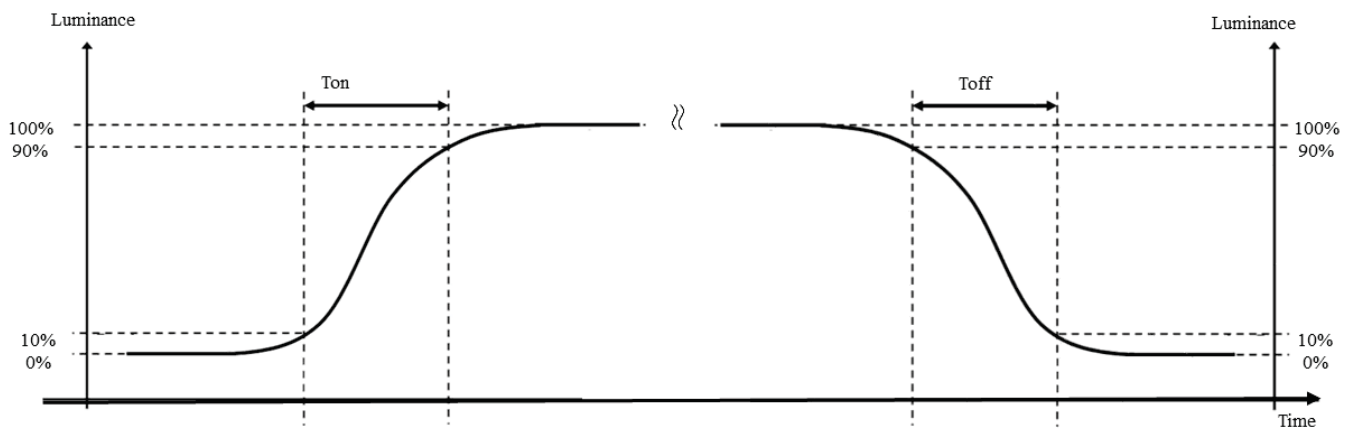


Fig. 6.4 The definition of T_{on} , T_{off}

All the transition time is measured at the center point of the LCD module by ELDIM OPTI Scope-SA.

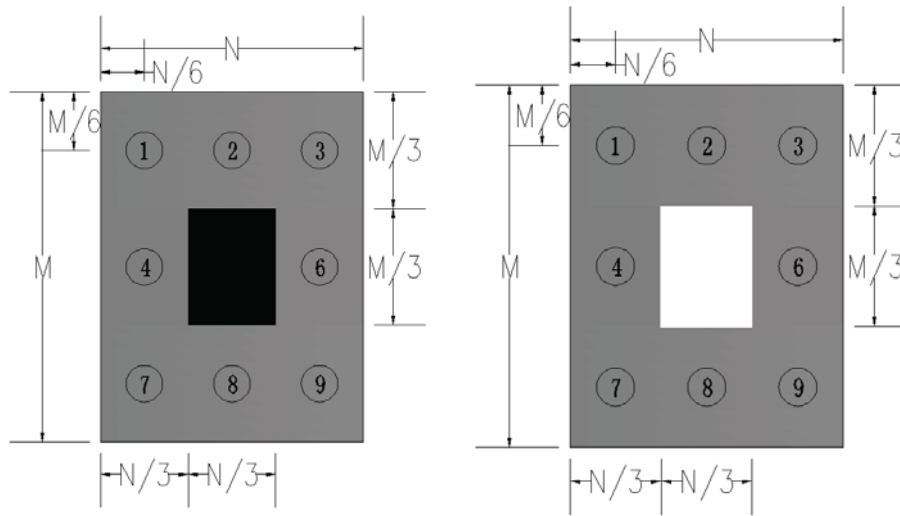
(4) Definition of center Transmittance (Tr %):

The transmittance is measured with full white pattern (Gray 255)

$$\text{Transmittance (Tr\%)} = \frac{\text{Luminance of LCD module}}{\text{Luminance of backlight}}$$

(5) Definition of the crosstalk:

- The point should be marked is, the background of Cross-talk Test Pattern-“gray “ are defined as 50% gray scale .
- $\Delta Bp_n = Bp_n (\text{gray}) / Bp_n (\text{white})$
- Which n means the dot No. In the Cross-talk Test Pattern ;
 $Bp_n (\text{gray})$ means the brightness of the No.n spots in Cross-talk Test Pattern;
 $Bp_n (\text{white})$ means the brightness of the No.n spots in Full white Test Pattern;
- $\Delta Bp (\text{Max.}) = \text{Maximum value in } \Delta Bp_1 \sim \Delta Bp_9, \text{ except the No. 5 spot.}$
- $\Delta Bp (\text{Min.}) = \text{Minimum value in } \Delta Bp_1 \sim \Delta Bp_9, \text{ except the No.5 spot.}$
- $\Delta CT = \Delta Bp (\text{Max.}) / \Delta Bp (\text{Min.}).$



Cross-talk Test Pattern

(6) Definition of color chromaticity:

Each chromaticity coordinates (x, y) are measured in CIE1931 color space when full-screen displaying primary color R, G, B and white. The color gamut is defined as the fraction in percent of the area of the triangle bounded by R, G, B coordinates and the area is defined by NTSC 1953 color standard in the CIE color space. Chromaticity coordinates are measured by CS2000 and the standard setup of measurement is shown in Fig. 7.6.

Product Specification

Colors

LMD

LCD Module

Fig. 6.6 The standard setup of color chromaticity measurement

(7) Definition of viewing angle coordinate system (θ_H , θ_V):

The contrast ratio is measured at the center point of the LCD module. The viewing angles are defined at the angle that the contrast ratio is larger than 10 at four directions relative to the perpendicular direction of the LCD module (two vertical angles: up θ_{V+} and down θ_{V-} ; and two horizontal angles: right θ_{H+} and left θ_{H-}) as illustrated in Fig. 7.7. The contrast ratio is measured by ELDIM EZ Contrast.

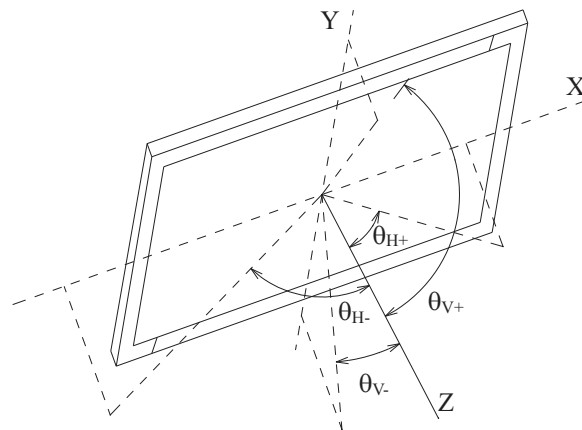


Fig. 6.7 Viewing angle coordination system

图纸编号

Technical drawing of a monitor bezel assembly, showing dimensions and specifications.

Front View Dimensions:

- Overall Width: 196 ± 0.8 Outline
- Overall Height: 368 ± 0.8 Outline
- Inner Display Area Width: 172 ± 0.5 A. A.
- Inner Display Area Height: 344.16 ± 0.5 A. A.
- Mounting Hole Spacing (Horizontal): 100
- Mounting Hole Spacing (Vertical): 48
- Resolution: 分辨率: 1920*960

Side View Dimensions:

- Thickness: 10 ± 0.8

Detail View Dimensions:

- Mounting Hole Diameter: $4-M3 \times 4$
- Mounting Hole Depth: 12.7 ± 0.8

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[illegible]

8.1 Packing Specifications and Methods

TBD

9. Definition of Labels

10. Precautions

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. We do not warrant the module, if customers disassemble or modify the module.

10.3 Breakage of LCD Panel

10.3.1. If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

10.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.

10.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

10.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.4 Electric Shock

10.4.1. Disconnect power supply before handling LCD module.

10.4.2. Do not pull or fold the CCFL cable.

10.4.3. Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

10.5 Absolute Maximum Ratings and Power Protection Circuit

10.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

10.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

10.5.3. It's recommended to employ protection circuit for power supply.

10.6 Operation

10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.

10.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

10.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

10.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may cause deformation or color fading.

10.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

10.7 Static Electricity

10.7.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

10.7.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

10.8 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

10.9 Disposal

When disposing LCD module, obey the local environmental regulations.