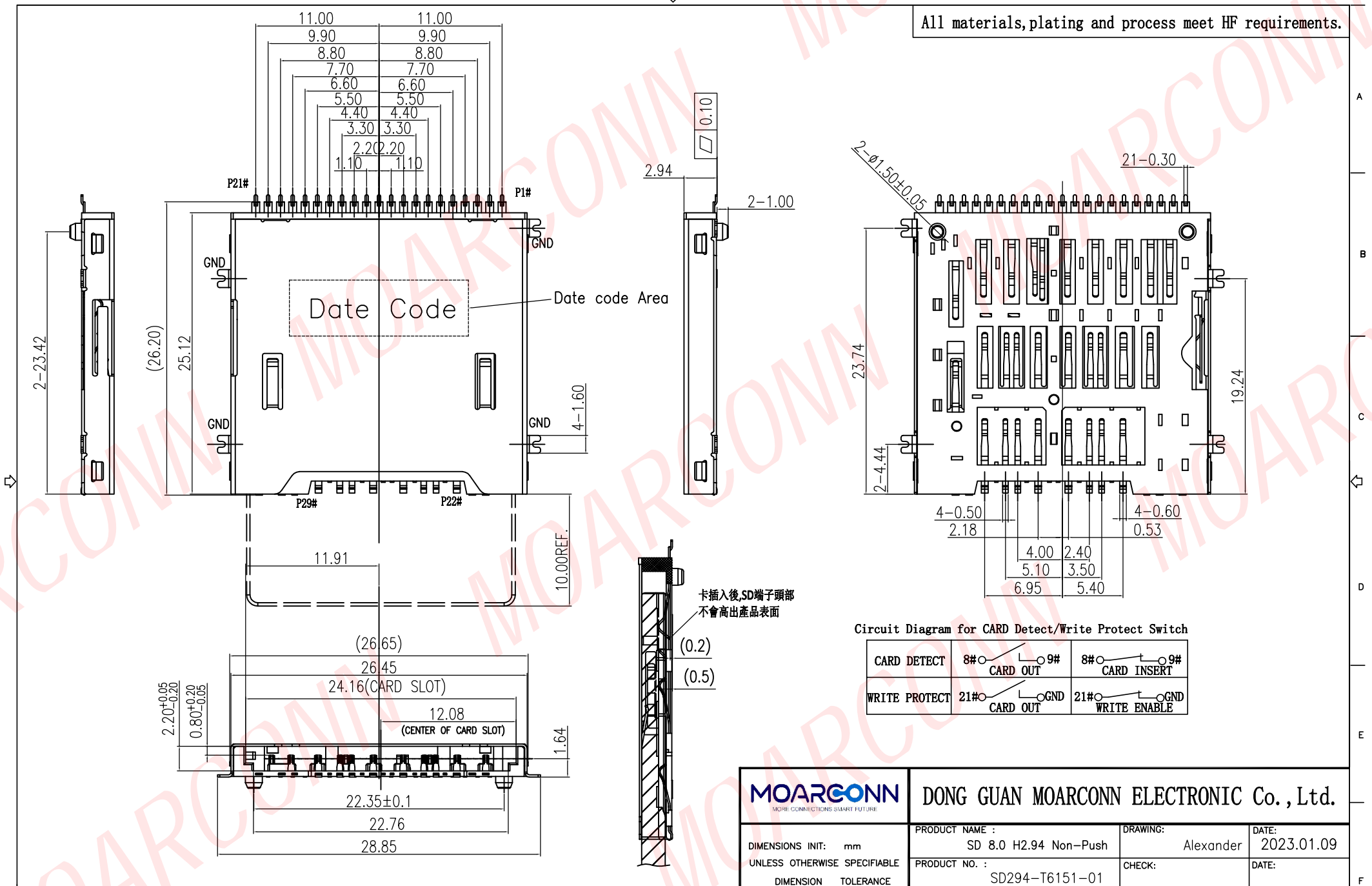
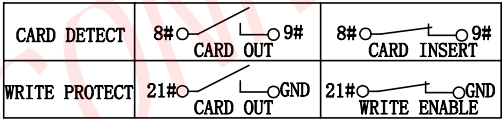


All materials, plating and process meet HF requirements.



卡插入後,SD端子頭部
不會高出產品表面

Circuit Diagram for CARD Detect/Write Protect Switch



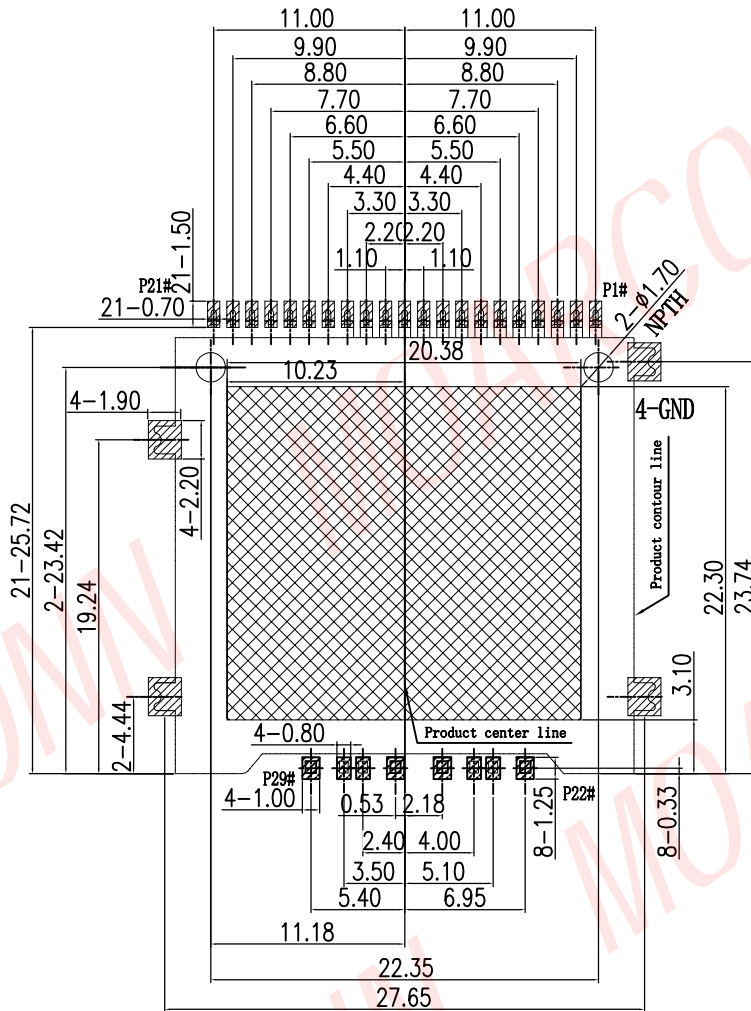
		DONG GUAN MOARCONN ELECTRONIC Co., Ltd.			
		PRODUCT NAME : SD 8.0 H2.94 Non-Push	DRAWING: Alexander	DATE: 2023.01.09	
DIMENSIONS INIT: mm		PRODUCT NO. : SD294-T6151-01	CHECK:	DATE:	
UNLESS OTHERWISE SPECIFIABLE		DRAWING NO. : D-SD29-T6151-01	APPROVED:	DATE:	
DIMENSION TOLERANCE		SCALE: 1:1		DWG ID: C D	REV.: X0
X.X: ± 0.35		PAGE: 1 OF 2			
X.XX: ± 0.20					
X.XXX: ± 0.10					
ANGULAR: ± 2'					

X0	----	NEW RELEASE	Alexander	2023.01.09
REVISION NO.	ECR NO.	DESCRIPTION	MARK	DATE

All materials, plating and process meet HF requirements.

Notes:

- Material:
 - Housing: LCP S475 Black
 - Terminal: C5210
 - Shell: SUS304-H
- Plating:
 - Terminal: 30u" Au Plated On Contact Area And G/F Plated On Solder Area
 - Shell: G/F Plated On Solder Area
- Operation Voltage: 10V AC
 - Current Rating: 1.0A MAX
 - Contact Resistance: 100mΩ MAX.
 - Dielectric withstanding voltage: 500V DC/1minute
 - Insulation Resistance: 1000MΩ Minutes. 500V DC
 - Operation Temperature: -20℃ +60℃
 - Storage Temperature: -20℃ +70℃
 - IR Reflow 260℃ 5s



RECOMMEND P.C.B LAYOUT
(General tolerance ±0.05)

PAD AREA
 NO COPPER AREA
 (NO Trace&Via&GND)

Pin Assignment				
No.	PIN#.	Name	Type	Description
P1	PIN10#	VSS3	S	Supply voltage ground
P2	PIN19#	VDD1a	S	Supply voltage (3.3V)
P3	PIN9#	CLKREQ#	I/O/OD	Reference clock request signal. Also used by L1 PM sub-states
P4	PIN11#	PCIe Tx0+	IDS	Card Input; lane 0
P5	PIN1#	PERST#	I	PE-Reset is a functional reset
P6	PIN12#	PCIe Tx0-	IDS	Card Input; lane 0
P7	PIN2#			Not Used
P8	CD PIN	CD		
P9	PIN3#	VSS1	S	Supply voltage ground
P10	PIN13#	VSS4	S	Supply voltage ground
P11	PIN14#	VDD2	S	Supply voltage (1.8V)
P12	PIN4#	VDD1	S	Supply voltage (3.3V)
P13	PIN15#	PCIe Rx0-	ODS	Card Output; lane 0
P14	PIN6#			Not Used
P15	PIN16#	PCIe Rx0+	ODS	Card Output; lane 0
P16	PIN6#	VSS2	S	Supply voltage ground
P17	PIN17#	VSS5	S	Supply voltage ground
P18	PIN7#	REFCLK+	IDS	PCIe Ref Clock
P19	PIN18#	VDD3	S	Supply voltage (1.2V)
P20	PIN8#	REFCLK-	IDS	PCIe Ref Clock
P21	WP PIN	WP		
P22	PIN20#	VSS6	S	Supply voltage ground
P23	PIN21#	PCIe Tx1+	IDS	Card Input; lane 1
P24	PIN22#	PCIe Tx1-	IDS	Card Input; lane 1
P25	PIN23#	VSS7	S	Supply voltage ground
P26	PIN24#	VDD2a	S	Supply voltage (1.8V)
P27	PIN25#	PCIe Rx1-	ODS	Card Output; lane 1
P28	PIN26#	PCIe Rx1+	ODS	Card Output; lane 1
P29	PIN27#	VSS8	S	Supply voltage ground

MOARCONN <small>MORE CONNECTIONS SMART FUTURE</small>	DONG GUAN MOARCONN ELECTRONIC Co., Ltd.				
	DIMENSIONS INIT: mm UNLESS OTHERWISE SPECIFIABLE DIMENSION TOLERANCE X.X: ± 0.35 X.XX: ± 0.20 X.XXX: ± 0.10 ANGULAR: ± 2'	PRODUCT NAME : SD 8.0 H2.94 Non-Push	DRAWING: Alexander	DATE: 2023.01.09	
	PRODUCT NO. : SD294-T6151-01	CHECK:	DATE:		
	DRAWING NO. : D-SD29-T6151-01	APPROVED:	DATE:		
		SCALE: 1:1	DWG ID: C D	REV.: X0	PAGE: 2 OF 2

X0	----	NEW RELEASE	Alexander	2023.01.09
REVISION NO.	ECR NO.	DESCRIPTION	MARK	DATE
1				
2				
3				