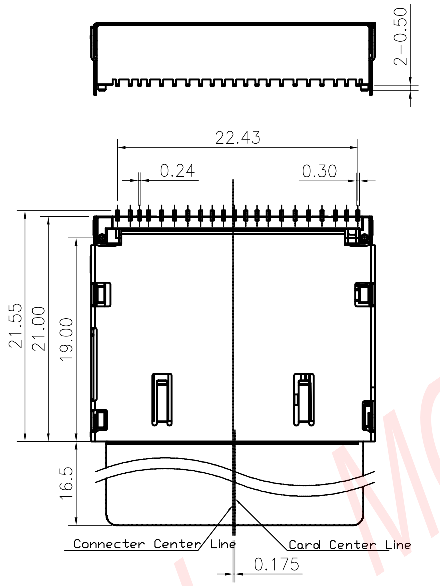
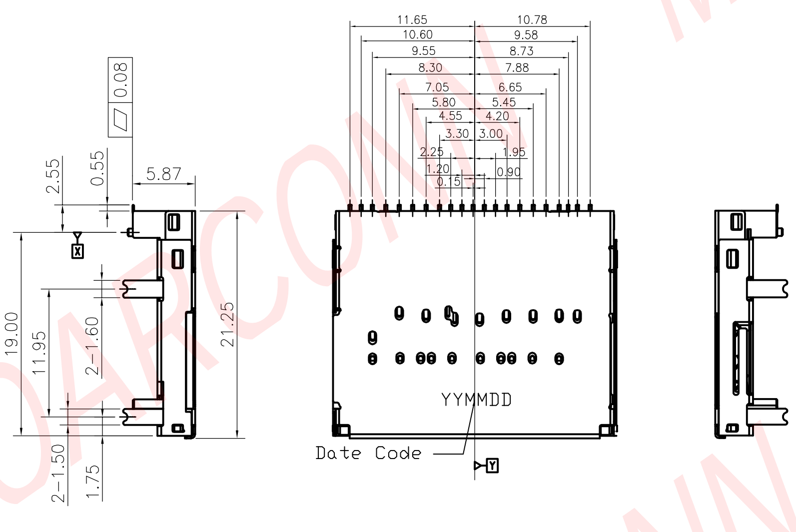
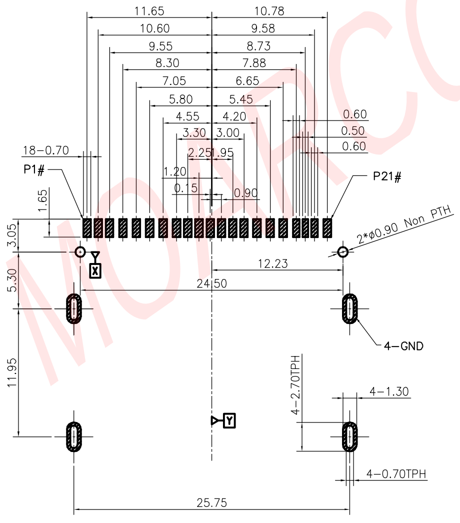
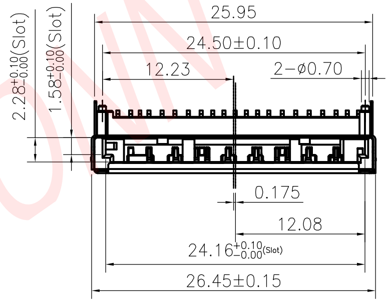
All materials, plating and process meet HF requirements.

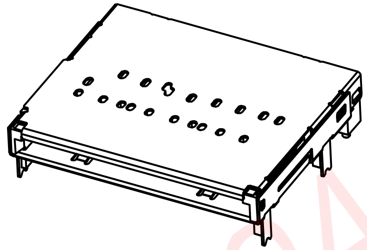






Recommended PCB Layout
Top View(Tolerance±0.05)

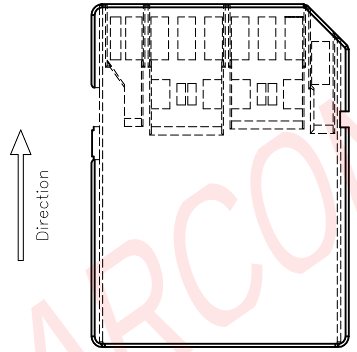




Card Uninsertion	P08 Open P09 P21 Open P09
Card Write Protect insertion	P08 Short P09 P21 Open P09
Card Normal insertion	P08 Short P09 P21 Short P09

Note:

1. Material:
   - 1-1. Plastic: LCP, Black, UL94V-0
   - 1-2. Contact: Coper Alloy
   - 1-3. Shell: Stainless Steel
2. Plating:
   - 2-1. Contact:
     - Contact Area: Au 1μ" min. Plating Over Ni 50μ" min.
     - Soldertail: Matte Tin 100μ" min. Plating Over Ni 50μ" min.
   - 2-2. Shell:
     - Solderability Ni 50μ" min. Over All
3. Contact And Tail Coplanarity To Be 0.10mm max.



PIN DEFINE:

SD Mode			UHS-Ⅱ Mode			SD Express Mode		
PIN#	NAME	DESCRIPTION	PIN#	NAME	DESCRIPTION	PIN#	NAME	DESCRIPTION
1			1	VSS3	Supply voltage ground	1	VSS3	Supply voltage ground
2			2			2	RESERVED	Reserved Area for future pad extension
3	DAT2	DATA LINE BIT2	3			3	CLKREQ#	Reference clock request signal. Also used by L1 PM sub-states
4			4	D0+	DIFFERENTIAL DATE 0+	4	PCIe TX+	
5	CD/DAT3	CARD DETECT/DATA LINE BIT3	5		NOT Used	5	PERST#	PE-Reset is a functional reset
6			6	D0-	DIFFERENTIAL DATE 0-	6	PCIe TX-	
7	CMD	COMMAND/RESPONSE	7		NOT Used	7		NOT Used
8	CD	CARD DETECT	8	CD	CARD DETECT	8	CD	CARD DETECT
9	VSS1	SUPPLY VOLTAGE GROUND	9	VSS1	Supply voltage ground	9	VSS1	Supply voltage ground
10			10	VSS4	Supply voltage ground	10	VSS4	Supply voltage ground
11			11	VDD2	Supply voltage(1.8V)	11	VDD2	Supply voltage(1.8V)
12	VDD	SUPPLY VOLTAGE	12	VDD1	Supply voltage(3.3V)	12	VDD1	Supply voltage(3.3V)
13			13	D1-	DIFFERENTIAL DATE 1-	13	PCIe RX-	
14	CLK	CLOCK	14		NOT Used	14		NOT Used
15			15	D1+	DIFFERENTIAL DATE 1+	15	PCIe RX+	
16	VSS2	SUPPLY VOLTAGE GROUND	16	VSS2	Supply voltage ground	16	VSS2	Supply voltage ground
17			17	VSS5	Supply voltage ground	17	VSS5	Supply voltage ground
18	DAT0	DATA LINE BIT0	18	RCLK+		18	REFCLK+	PCIe Ref CLOCK
19			19			19	VDD3	Supply voltage(1.2V)-TBD
20	DAT1	DATA LINE BIT1	20	RCLK-		20	REFCLK-	PCIe Ref CLOCK
21	WP	WRITE PROTECT	21	WP	WRITE PROTECT	21	WP	WRITE PROTECT

X0	----	NEW RELEASE	Aaron	2022.04.01
REVISION NO.	ECR NO.	DESCRIPTION	MARK	DATE

MOARCONN
MORE CONNECTIONS SMART FUTURE

DONG GUAN MOARCONN ELECTRONIC Co., Ltd.

DIMENSIONS INIT: mm
UNLESS OTHERWISE SPECIFIABLE

DIMENSION	TOLERANCE
X.X: ±	0.35
X.XX: ±	0.20
X.XXX: ±	0.10
ANGULAR: ±	2°

PRODUCT NAME : SD7.0 BOTTOM H5.87 Non-push	DRAWING: Aaron	DATE: 2022.04.01
PRODUCT NO. : SD587-D1250-01	CHECK:	DATE:
DRAWING NO. : D-SD587-D1250-01	APPROVED:	DATE:

SCALE:	DWG ID:	REV.:	PAGE:
1:1	C D	X0	1 OF 1