Specification for KYOCERA KCB104VG2BA-A21

Sec-No.: TQ3C-8EAC0-E1ABYM69

Date: October 1998

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SPECIFICATION

KCB104VG2BA-A21

APPROVED BY	 ME PRINTED/TITLE)
SIGNATURE	
DATE	

Volume Run

Issued	
Date: JUL	
KADCE	Ra
Hayato LCD	Division.

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Designo	ed by :Engineeri	Confirmed b	y :QA Dept.		
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SPEC. NO.	TQ3C-8EACO-E1ABYM69-01
DATE	October 22,1998

SPEC

TYPE: KCB104VG2BA-A21

CONTENTS

- 1. Application
- 2. Construction and Outline
- 3. Mechanical Specifications
- 4. Absolute Maximum Ratings
- 5. Electrical Characteristics
- 6. Optical Characteristics
- 7. Circuit Block Diagram
- 8. Interface Signals
- 9. Interface Timing Chart
- 10. Data and Screen
- 11. Input Timing Characteristics
- 12. Supply Voltage Sequence Condition
- 13. Backlight Characteristics
- 14. Lot Number Identification
- 15. Warranty
- 16. Precautions for Use
- 17. Reliability Data / Environmental Test
- 18. Outline Drawing

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Revision Record

					Kecord				
Date		Des	igned	by : Engineer		Confirmed by			
Date		Prepa		Checked	Approved	Checked	Approved		
October 22,	1998	J. Ysay	opi	M. Twiltani	a historio	S. Hayashi	y. Justida		
Rev. No.	D	ate	Page	Descriptions					
01	Oct.	22, 1998	-	Cover page \sim Delete Comment "This specification \sim ."					
			17	13-2. Surface ~ Delete "(Brightness of)"	LCD			
			19	16 Precacaut ~ Add "16-6	ions for use . Do not pull	the CFL lead w	ires		

Caution

- This LCD is supposed to be for general electric appliances such as audio, office automation, industrial applications, home appliances and game machines. Do not use the LCD as a display for a medical instrument that is required extremely high reliability and its failure and malfunction may affect human lives. In the case you did, Kyocera will not take responsibility for it.
- Kyocera may scrap the tooling or stop supplying the LCD, after 2 year time frame from your latest purchase of the LCD.

1. Application

This data sheet defines the specification for a $(640 \times 3) \times 480$ dot, STN color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

(640×3)×480 dots, COB type LCD with CFL backlight.

Backlight system

: Side-edge type CFL (2 tubes).

Inverter

: Option.

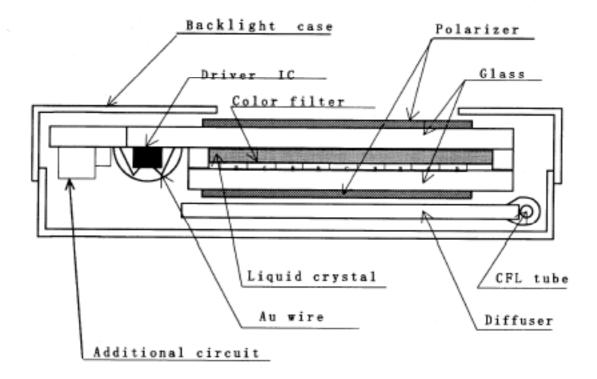
Recommended Inverter : KCI-13 (MINEBEA ELECTRONICS)

or equivalent.

Polarizer

: Non-Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit, DC/DC converter.



3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	264.0 (W) × 183.0 (H) × 8.5 (D)	nn.
Effective viewing area	215.07 (W) × 162.27 (H)	, mn
Dot number	(640×3) (₩) × 480 (H)	Dots
Dot size	0.09 (W) × 0.31 (H)	101
Dot pitch	0.11 (W) × 0.33 (H)	
Display color *1	♥hite *2	-
Base color *1	Black *2	_
Weight	540	g

- *1 Due to the characteristics of the LC material, the color vary with environmental temperature.
- *2 Negative-type display

Display data "H" :R,G,B Dots ON : White Display data "L" :R,G,B Dots OFF : Black

4. Absolute Maximum Ratings

4-1 Electrical absolute maximum ratings

Temp. = 25 ℃

ITEM	SYMBOL.	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	6.0	v
Supply voltage for LCD driving	VCONT	0	VDD	v
Input voltage	Vin	0	VDD+0.3	v

4-2 Environmental absolute maximum ratings

ITEN	SYMBOL	MIN	MAX	UNIT
Operating temperature	Тор	0	40	°C
Storage temperature *1	Тето	-20	60	°C
Operating humidity *2	Нор	10	85	%RH
Storage humidity *2	Нато	10	*3	%RH
Vibration	-	*4	*4	_
Shock	_	*5	*5	_

- *1 Temp. = -20% < 24 Hr. , Temp = 60% < 24 Hr. No vibration and shock.
- *2 Non-condensation.
- *3 Temp. ≤ 40°C, 85% RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85%RH at 40°C.

Frequency	10∼55 Hz	Converted to acceleration value
Vibration width	0.15 nm	(0.03~0.916)
Interval	10-55-1	0 Hz 1 minute

2 hours in each direction X/Y/Z (6 hours as total) EIAJ ED-2531.

*5 Acceleration: 50 G

Pulse width: 11 msec

3 times in each direction : $\pm X/\pm Y/\pm Z$.

EIAJ ED-2531.

5. Electrical Characteristics

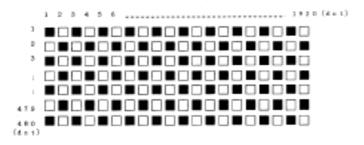
Temp. = 25°C, VDD = +5.0V ± 5 %

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	_	4.75	5.00	5. 25	v
LCD driving voltage *1	Vop=	0 ℃	0.80	_	_	v
	VCONT	25 °C	1.35	1.95	2.55	v
		40 °C	-	_	2. 80	v
Input voltage	Vin	"H" level	o. 8VDD	_	VDD	v
		"L" level	0	-	0. 2VDD	v
Clock frequency	f cp		4.03	4.32	18. 0	MHz
Frame frequency *2	fram		70	75	80	Hz
Current consumption for logic	IDD	*3	-	68	102	nA.
Power consumption	Pdisp		-	340	510	mW.

- *1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop= VCONT) for driving LCD.
- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.
- *3 Display high frequency pattern, (see below).

 VDD = 5.0V , Vop = VCONT , f PRM = 75 Hz , fcp = 4.32MHz

 Pattern:



6. Optical Characteristics

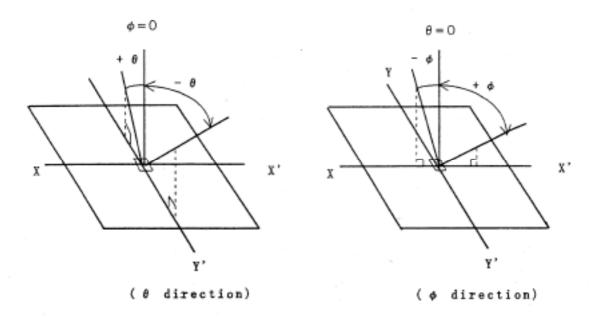
Measuring Spot = φ6mm , Temp. = 25℃

ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT
Response	Rise	Tr	θ = φ =0°		-	160	260	ns
time	Down	Td	θ = φ	=0°	-	80	180	ns
Viewing angle range		θ	CB > 0	φ=0"	-20	-	35	deg.
		φ	CR≥2	θ =0°	-45	_	45	deg.
Contrast rat	io	CR	θ = φ	=0°	10.0	20. 0	_	ı —
Chromaticity	Red	х	$\theta = \Phi = 0_{\phi}$		0.49	0.54	0.59	
coordinates		у			0.29	0.34	0.39	
	Green	x	θ = φ =0°		0.25	0.30	0.35	
		у	θ = φ	=0	0.50	0.55	0.60	
	Blue	x	θ = φ	-00	0. 12	0.17	0.22	
		у	0 - q	-0	0. 10	0.15	0.20	
	White	x	0 - 4	-0°	0.26	0.31	0.36	
		у	$\theta = \phi = 0^{\circ}$		0.28	0.33	0.38	
	Black	x	0 = 1	-02	0.27	0.32	0.37	
		, y	$\theta = \phi$	-0	0.29	0.34	0.39	

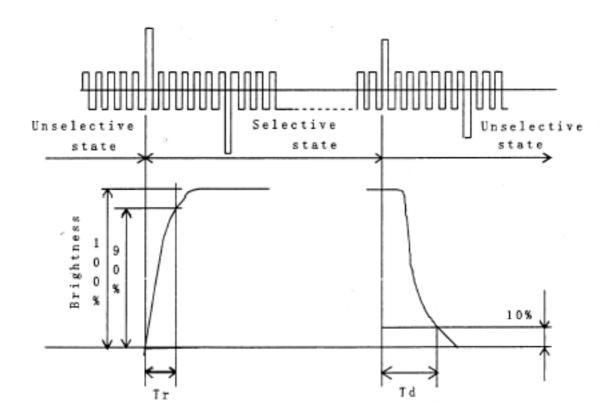
Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of θ = ϕ = 0° .

6-1 Contrast ratio is defined as follows:

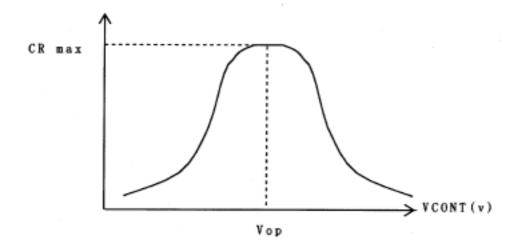
6-2. Definition of viewing angle



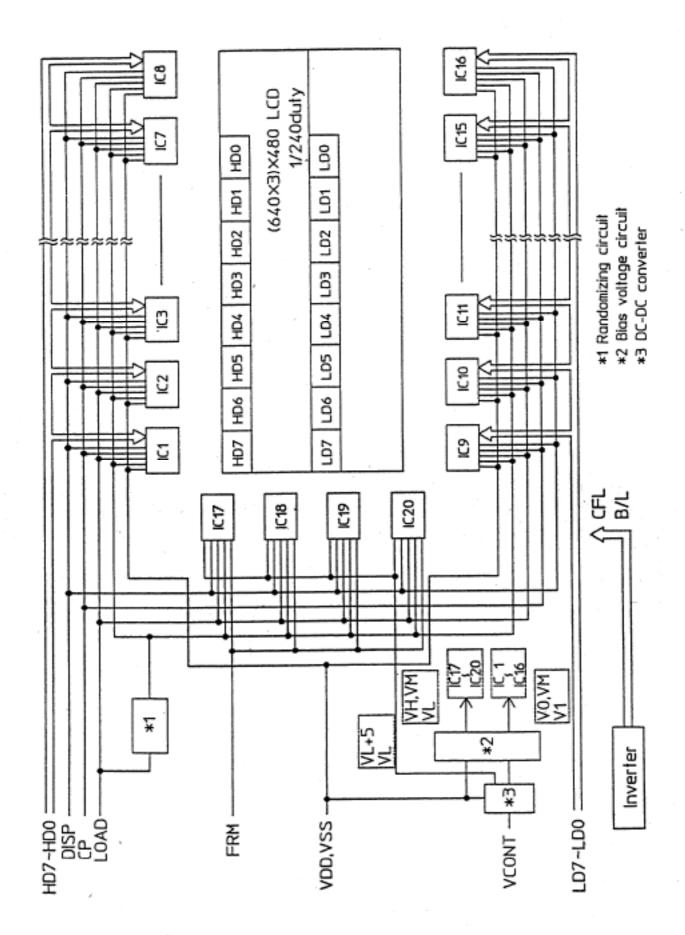
6-3. Definition of response time

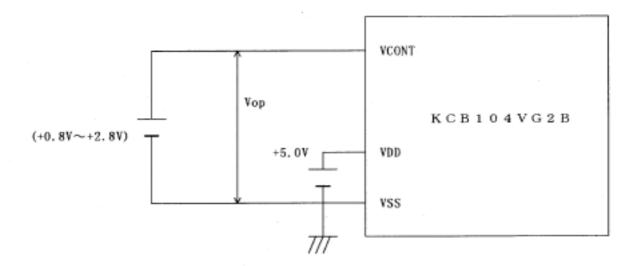


6-4. Definition of Vop



7. Circuit Block Diagram





8. Interface Signals

8-1. LCD

CN1 : 53261-1510(Molex)

		Y	
PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	Н
2	NC	No connect	_
3	DISP	Display control signal	H(ON), L(OFF)
4	LOAD	Data signal latch clock	$H \rightarrow \Gamma$
5	VSS	GND	
6	CP	Data signal shift clock	$H \rightarrow L$
7	VSS	GND	_
8	HDO	Display data (Upper column)	H(ON), L(OFF)
9	HD1		
10	HD2		
11	HD3	,	
12	HD4		
13	HD5		
14	HD6		
15	HD7		

Recommended matching connector : 51021-1500 (Molex)

CN2 : 53261-1410(Molex)

PIN No	SYMBOL	DESCRIPTION	LEVEL
1	LD0	Display data (Lower column)	H(ON), L(OFF)
2	LD1		
3	LD2		
4	LD3		
5	LD4	·	
6	LD5		
7	LD6		
8	LD7		
9	VDD	Power supply for logic	
10	VSS	GND	_
11	NC	No connect	
12	NC	No connect	
13	NC	No connect	
14 ·	VCONT	LCD adjust voltage	

Recommended matching connector : 51021-1400 (Molex)

8-2. CFL

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	GND	Ground line (from inverter)	
2	NC	No connect	
3	HV	Power supply for CFL	AC

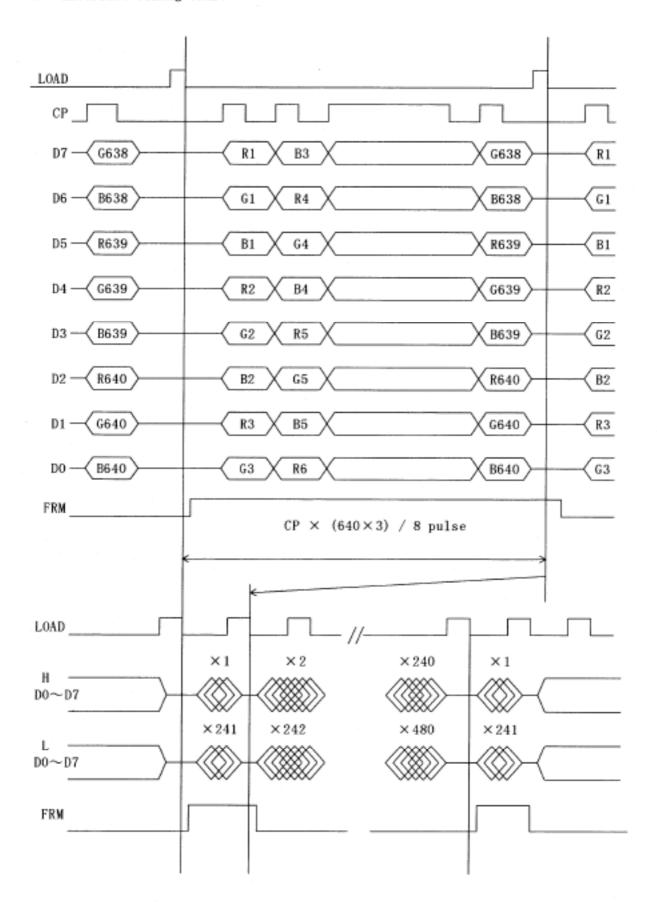
LCD side connector

: QZ-19-3F01

 $(HONDA) \times 2$

Recommended matching connector : QZ-19-A3MYL#02 (HONDA) \times 2

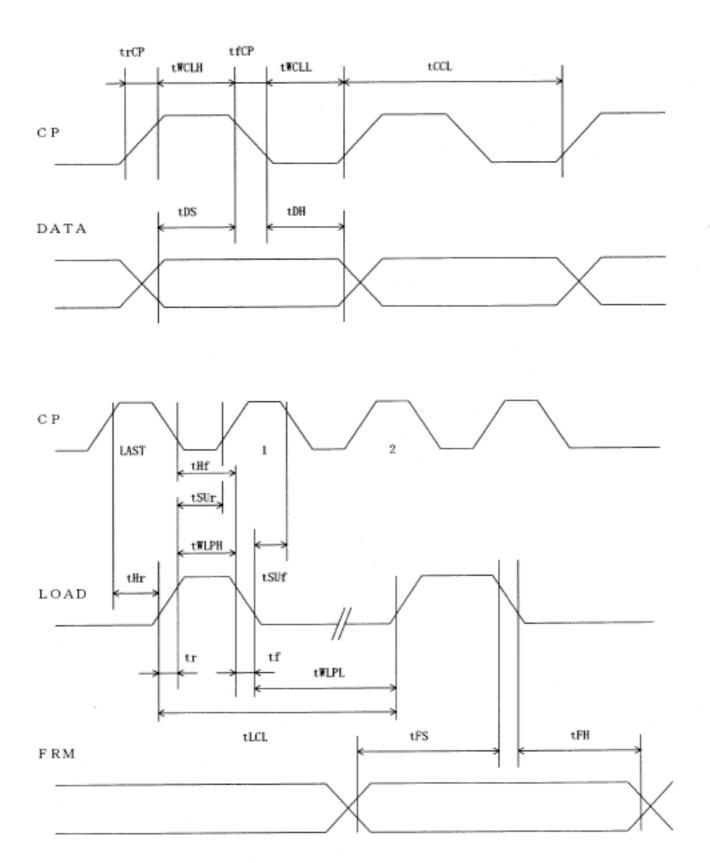
9. Interface Timing Chart



10. Data and Screen

							CI	HIP ARI	EΛ				
			Y1			¥2			У 3			Y640	
C H I	X1 X240	HD7 R1	HD6 G1	HD5 B1	HD4 R2	HD3 G2	HD2 B2	HD1 R3	HD0 G3	HD7 B3	HD2 R640	HD1 G640	HD0 B640
	X241										 		
R R	X480	LD7 R1	LD6 G1	LD5 B1	LD4 R2	LD3 G2	LD2 B2	LD1 R3	G3	LD7 B3	LD2 R640	LD1 G640	LD0 B640
							Cl	HIP AR	EA		 		

1 1. Input Timing Characteristics



11-1. Switching characteristics

Input characteristics ; VDD = +5.0V \pm 5%

Temp. = 25℃

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	56	-	ns
CP "H" Pulse Width	tWCLH	15	_	ns
CP "L" Pulse Width	t₩CLL	15	-	ns
CP Rise Up Time *2	trCP	-	50	ns
CP Fall Down Time *2	tfCP	, , , , , , , , , , , , , , , , , , ,	50	ns
Data Set Up Time	tDS	10	_	ns
Data Hold Time	tDH	10	_	ns
Load "H" Pulse Width	tWLPH	100	_	ns
Load "L" Pulse Width	tWLPL	100	_	ns
LOAD Cycle	tLCL	10	-	μS
Data Strobe Set Up Time	tSUr	20	_	ns
Data Strobe Set Up Time	tSUf	20	-	ns
Data Strobe Hold Time	tHr	5	_	ns
Data Strobe Hold Time	tHf	20	_	ns
Input Signal Rise Up Time	tr	_	30	ns
Input Signal Fall Down Time	tf	_	30	ns
FRM Data Set Up Time	tFS	100	_	ns
FRM Data Hold Time	tFH	100	-	ns

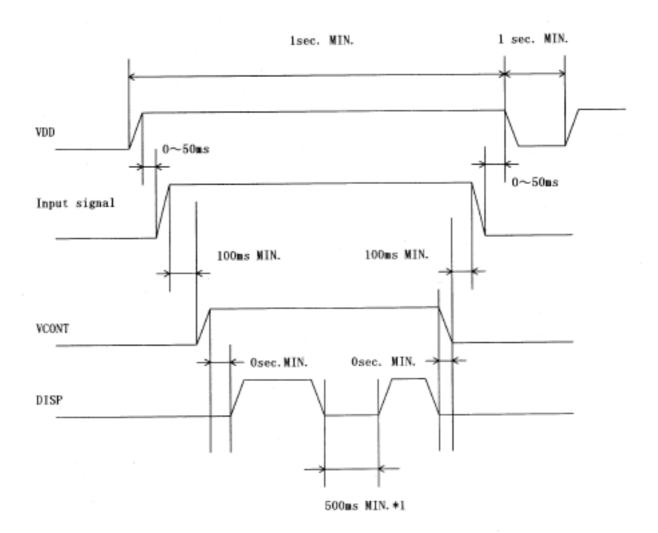
^{*1} CP Cycle is adjust so that FRM signal is under 75 Hz.

① trCP, tfCP < {tCCL - (tWCLH + tWCLL)) / 2 Please use on condition that ① is filled.

^{*2} The formula of condition

12. Supply Voltage Sequence Condition

<u>DO NOT</u> apply DC voltage to the LCD panel. DC voltage induce irreversible electrochemical reactions and reduce LCD life. Always follow the power supply ON/OFF sequence of VDD first, input signal second, VCONT third and finally DISP. This will prevent DC driving of the LCD or CMOS LSI latch up as shown below.



- *1 Take interval time for minimum 500ms once you cut off the Disp signal.
- Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

13 . Backlight Characteristics

13-1. CFL ratings

Measuring Inverter : KCI-13 (MINEBEA ELECTRONICS)

Temp. = 25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting		-	-	1,360 Vrms.	0 ℃
discharge Voltage *1	VS	_	_	905 Vrms.	25 ℃
Discharging tube current	ΙL	2.0 mArms.	5.0 mArms.	6.0 mArms.	_
Discharging tube voltage	VL	-	540 Vrms.	_	-,
Operating life *2 (IL=5.0 mArms.)	Т	40,000 Hr.	60,000 Hr.	_	-
Operating frequency	F	40 kHz	-	100 kHz	_

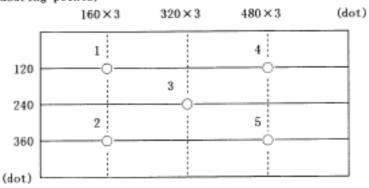
- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value : 1,768 Vrms MIN.)
- *2 When the illuminance or quantity of light has decreased to 50 % of the initial value.

13-2. Surface Brightness of LCD (IL = 5.0 mArms. × 2)

Temp. =25℃

ITEM	MIN.	TYP.	MAX.	UNIT
Brightness	100	150	-	ed/m²

(Measuring points)



- 1) Rating is defined as the average brightness inside the viewing area.
- 30 minutes after CFL is turned on. (Ambient Temp. =25°C)
- The inverter should meet the eccentric conditions;
 - -Sine, symmetric waveform without spike in positive and negative.

14. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



YEAR	1998	1999	2000	2001	2002	2003
CODE	8	9	0	1	2	3

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	x	Y	Z

15. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

16. Precautions for use

16-1. Installation of the LCD

- Please ground either of the mounting(screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.

16-2. Static Electricity

 Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

16-3. LCD Operation

- The LCD shall be operated within the limits specified. Operation at values outside
 of these limits may shorten life, and/or harm display images.
- 2. Vop must be adjusted to optimize viewing angle and contrast.
- Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal. <u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

16-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

16-5. Screen Surface

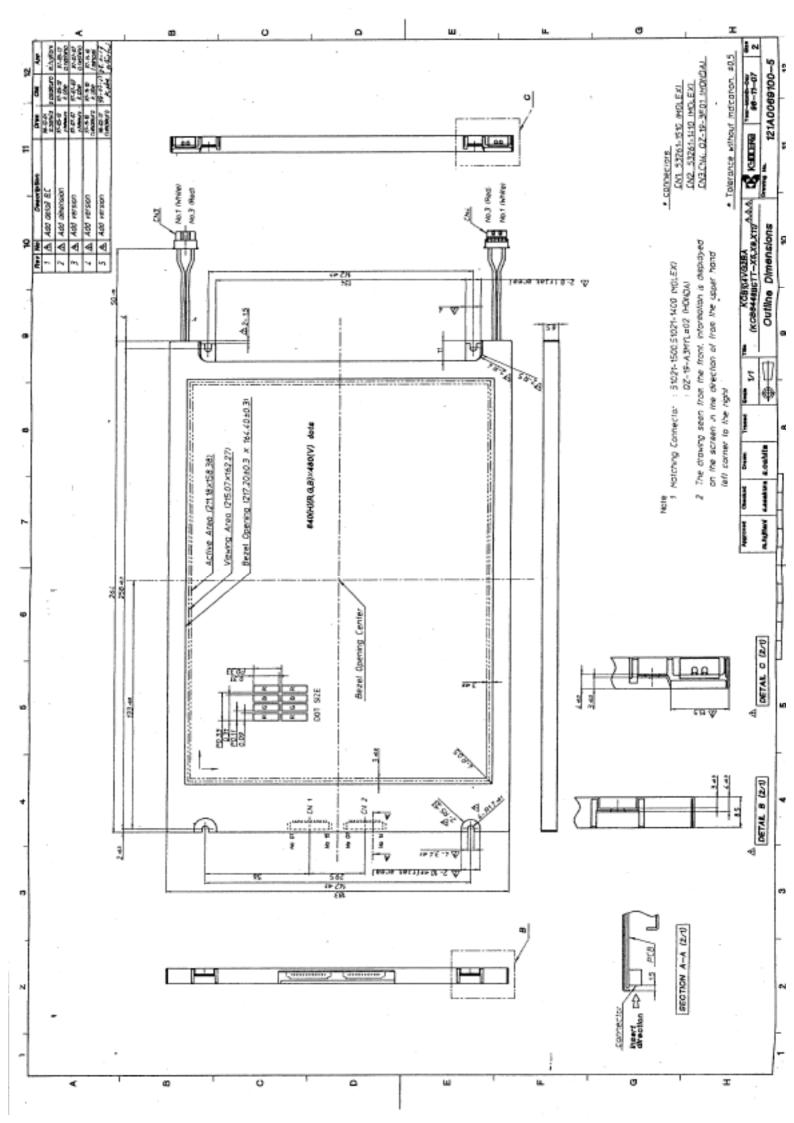
- <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

17. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST	RESULT
High Temp. Atmosphere	70°C	240 Hr.	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-20°C	240 Hr.	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90%RH	240 Hr.	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20°C 0.5 Hr. R.T. 0.5 Hr. 70°C 0.5 Hr.	10cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50°C Vop	500 Hr.	Display Quality : No defect Current Consumption : No defect

- * Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- * The LCD is tested in circumstances in which there is no condensation.
- * The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.



SPEC. NO.	TQ3C-8EACO-E2ABYM69-00
DATE	July 1,1998

FOR: DATA MODUL

KYOCERA INSPECTION STANDARD

TYPE : KCB104VG2BA-A21

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Revision Record

Date		Des	signed	by : Engineer	ing Dept.	Confirmed by	: QA Dept.
Date.		Prepa	red	Checked	Approved	, Checked	Approved
Rev. No.	D	ate	Page		Descri	ptions	
				,			

Visuals specification

1)Note

Item	Note		
General	1. When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where the defect is observed most clearly. Display quality is applied up to effective viewing area. (Gray-Scale INSPECTION) 2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 3. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera. 4. Inspection conditions Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 ℃ Direction : right above		
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.	
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.	
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.	
		5 5 6 7 8 8	

2)Standard

Inspection item	Judgement standard				
Pinhole, Bright spot Black spot, Foreign particle	d = (a + b) / 2				
	Category Size	(nm)	Acceptab	le number	
		1 ≤ 0.2		lected	
		1 ≤ 0.3		5	
	C 0.3 <	1 ≤ 0.5		3	
	D 0.5 <			0	
	L				
	Width (nm)	Len	gth (nm)	Acceptable No.	
	A ₩ ≤ 0.03			neglected	
,	В		L ≤ 2.0	neglected	
	C 0.03 < ¥ ≤ 0.1	2.0 <	C L ≦ 4.0	3	
	D	4.0 <	C L	0	
	E 0.1 < W		_	According to Circular	
Contrast variation					
	d = (a + b) / 2				
	Category Size (mm)		Acceptable number		
		1 ≦ 0.5		lected	
		1 ≤ 0.7		3	
	C 0.7 <			0	

Inspection item

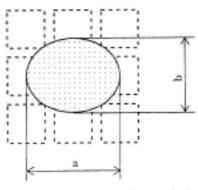
Judgement standard

Polarizer (Scratch, Bubble, Dent) (1) Scratch



	Width (mm)	Length (mm)	Acceptable No.
A	₩ ≦ 0.1	_	neglected
В		L ≦ 5.0	neglected
С	0.1 < ₩ ≦0.3	5.0 < L	0
D	0.3 < W	_	0

(2)Bubble (dent)



d = (a + b) / 2

Category	Size (nm)	Acceptable number
A	d ≦ 0.2	neglected
В	0.2 < d ≤ 0.3	5
С	0.3 < d ≦ 0.5	3
D	0.5 < d	0