



# Test Report: DDR-240B-48

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240W DIN Rail Type DC-DC Converter

## ■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Control Function Test

Component Stress Test

## ■ SAFETY & E.M.C. TEST

Safety Test

E.M.C. Test

## ■ RELIABILITY TEST

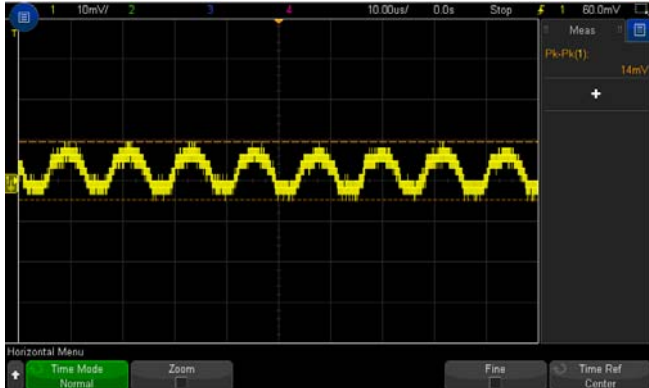
ENVIRONMENT TEST

## DESIGN VERIFY TEST

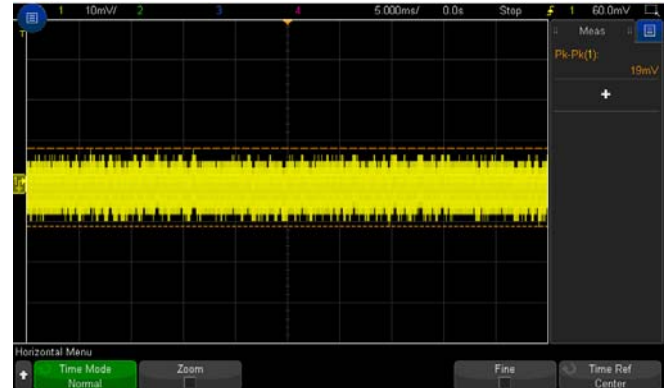
### OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OUTPUT VOLTAGE ADJUST RANGE	CH1: 48V~56 V	I/P:NORMAL VOLTAGE O/P:MIN LOAD Ta:25°C	CH1: 45.95V~ 58.13V
2	OUTPUT VOLTAGE TOLERANCE (Max)	V1: -1%~1 %	I/P: 16.8VDC /33.6VDC O/P:FULL/ MIN. LOAD Ta:25°C	V1: -0.1%~ 0.05%
3	LINE REGULATION (Max)	V1: -0.5 %~ 0.5%	I/P: 16.8 VDC /33.6VDC O/P:FULL LOAD Ta:25°C	V1: 0%~ 0.03%
4	LOAD REGULATION (Max)	V1: -1 %~ 1 %	I/P: 24VDC O/P:FULL ~MIN LOAD Ta:25°C	V1: -0.1%~ 0.05%
5	OVER/UNDERSHOOT TEST	< ±5%	I/P: 24 VDC O/P:FULL LOAD Ta:25°C	TEST:1.2%
6	RIPPLE & NOISE (Max)	V1:100mVp-p	I/P:24VDC O/P:FULL LOAD Ta:25°C	V1: 19mVp-p

high frequency :



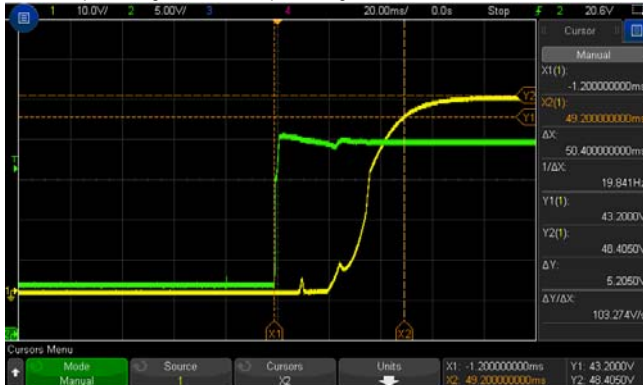
low frequency :




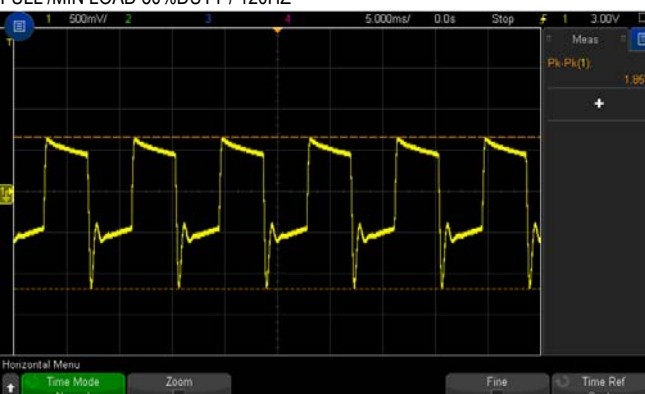
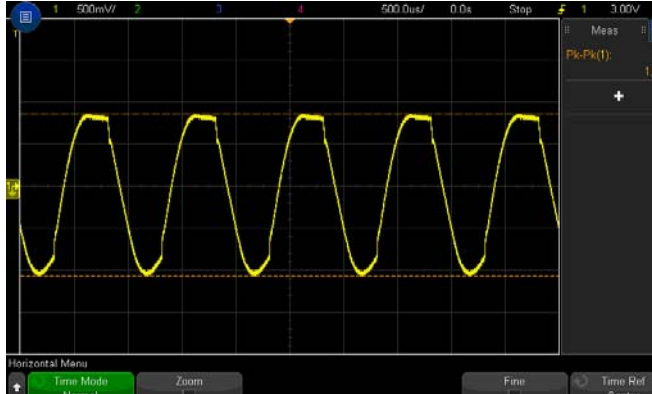


7	SET UP TIME (Max)	24VDC/ 500 ms	I/P: 24VDC O/P:FULL LOAD Ta:25°C	24VDC/ 50.4ms
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INPUT=24VDC @ FULL LOAD

CH1 : Output Voltage CH2 : DC Input Voltage

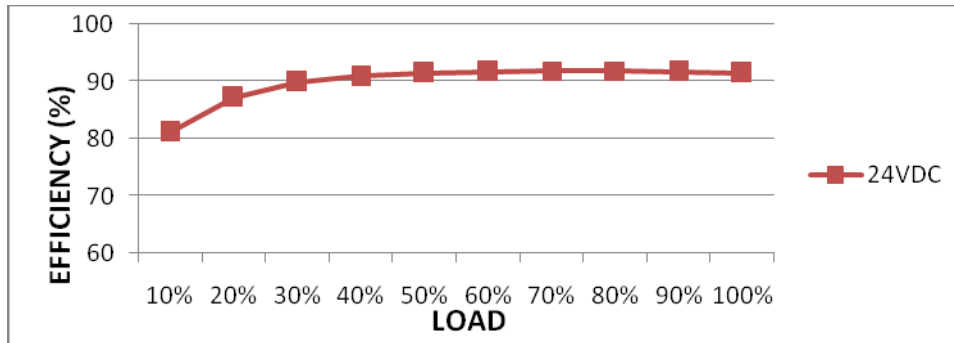


8	RISE TIME (Max)	24VDC/ 60ms	I/P: 24 VDC O/P:FULL LOAD Ta:25°C	24VDC/ 24.32ms
<p>INPUT=24VDC @ FULL LOAD</p> 				
9	HOLD UP TIME (TYP)	24VDC/6ms 24VDC/10ms@70%LOAD	I/P: 24 VDC O/P:FULL LOAD Ta:25°C	24VDC/ 7.6ms 24VDC/ 11.0ms@70%LOAD
<p>INPUT=24VDC @ FULL LOAD CH1 : Output Voltage CH2 : DC Input Voltage</p>  <p>INPUT=24VDC @ 70% LOAD CH1 : Output Voltage CH2 : DC Input Voltage</p> 				
10	DYNAMIC LOAD	V1: 4800mVp-p	I/P: 24VDC O/P: (1)FULL /MIN LOAD 50%DUTY / 120HZ (2)FULL /MIN LOAD 50%DUTY / 1KHZ Ta:25°C	1850mVp-p 1930mVp-p
<p>FULL /MIN LOAD 50%DUTY / 120HZ</p>  <p>FULL /MIN LOAD 50%DUTY / 1KHZ</p> 				

INPUT FUNCTION TEST

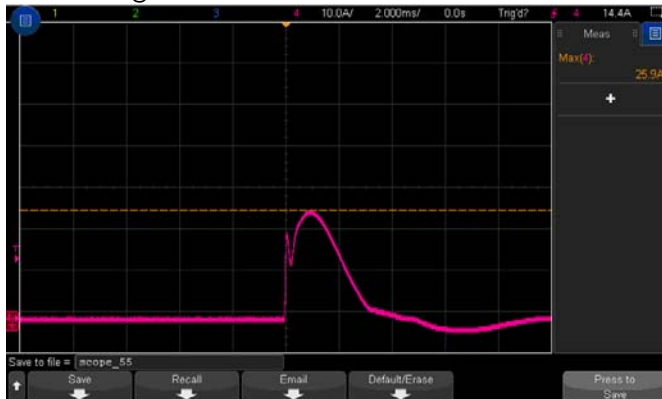
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	16.8VDC~ 33.6VDC	I/P: TESTING O/P: FULL LOAD Ta: 25°C	15.867V~ 33.6V
			I/P: LOW-LINE-0.2=16.6V HIGH-LINE+3V=36.6V O/P: FULL/MIN LOAD (PLEASE CHECK DERATING CURVE) ON: 30 Sec . OFF: 30 Sec 10MIN ( POWER ON/OFF NO DAMAGE )	TEST: OK
2	INPUT CURRENT(TYP)	24VDC/11.2 A	I/P: 24VDC O/P: FULL LOAD Ta: 25°C	I = 10.89A/24VDC
3	EFFICIENCY(TYP)	90%	I/P: VDC O/P: FULL LOAD Ta: 25°C	90.92%

EFFICIENCY vs LOAD



4	INRUSH CURRENT(TYP)	24VDC/30 A COLD START	I/P: 24 VDC O/P: FULL LOAD Ta: 25°C	I = 25.9A/ 24VDC
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INPUT=24VDC @ FULL LOAD



**PROTECTION FUNCTION TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	105 %~135 %RATED OUTPUT POWER	I/P: 33.6VDC I/P: 24 VDC I/P: 16.8 VDC O/P: TESTING Ta:25°C	127.5%/ 33.6 VDC 126.9%/ 24 VDC 127.0%/ 16.8 VDC PROTECTION TYPE : Normally works within 150% rated output power for more than 3 seconds and then constant current protection 105~135% rated output power with auto-recovery
2	OVER VOLTAGE PROTECTION	CH: 57.6V~ 65.0V	I/P: 33.6 VDC I/P: 24VDC I/P: 16.8 VDC O/P: MIN LOAD Ta:25°C	62.1V/ 33.6 VDC 62.1V/ 24 VDC 62.5V/ 16.8 VDC PROTECTION TYPE : Shut down O/P voltage, re-power on to recover
3	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P:33.6VDC O/P: FULL LOAD Ta:25°C	NO DAMAGE PROTECTION TYPE : constant current protection 105~135% rated output power with auto-recovery
4	INPUT REVERSE	POWER OK	I/P: 33.6VDC O/P: NO LOAD Ta:25°C	NO DAMAGE

**COMPONENT STRESS TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Transistor ( D to S) or (C to E) Peak Voltage	Q11 Rated : 100 V	I/P:High-Line +3V =36.6V DC ON/OFF VDS: O/P: (1)Full Load (2)Output Short (3)Full Load Continue Ta:25°C	VDS: (1)60.6V (2)47.0V (3)52.6V
2	Clamp MOSFET ( D to S) or (C to E) Peak Voltage	Q 6 Rated : 100V	I/P:High-Line +3V =36.6V DC ON/OFF VDS: O/P: (1)Full Load (2)Output Short (3)Full load Continue Ta:25°C	VDS: (1) 58.2V (2) 38.9V (3)52.6 V
3	Diode Peak Voltage	D100 Rated : 20A/ 400V  D102 Rated : 20A/ 400V	I/P:High-Line +3V =36.6V DC ON/OFF O/P: (1)Full Load (2)Output Short (3)Full Load Continue Ta:25°C	D100: (1)275V (2)68.7V (3)93.6V  D102: (1)323V (2)267V (3)331V
4	Input Capacitor Voltage	C7 Rated : 2200 $\mu$ / 35V	I/P:High-Line +3V =36.6V O/P: (1)Full Load input on/off (2) Min load input on /Off (3)Full Load /Min load Change (4)Full load continue Ta:25°C	(1) 34.2V (2)34.8V (3)34.2V (4)34.2v

5	Control IC Voltage Test	PWM IC U1 Rated -0.3V~16V	I/P:High-Line +3V =36.6V DC ON/OFF O/P(1)FULL LOAD (2) Output Short (3)O.L.P (4)O.V.P. (5)NO LOAD VR 下限.LOW LINE Ta:25°C	(1) 14.0V (2) 7.9V (3) 14.0V (4) 13.5V (5) 9.3V
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## ■ SAFETY & E.M.C. TEST

### SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	EN 60950-1 I/P-O/P:4KVDC/min I/P-FG:2.5 KVDC/min O/P-FG:0.71KVDC/min	I/P-O/P: 4.4KVDC/min I/P-FG: 3 KVDC/min O/P-FG:0.85KVDC/min Ta:25°C	I/P-O/P: 0 mA I/P-FG: 0 mA O/P-FG: 0 mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>100MΩ I/P-FG: 500VDC>100MΩ O/P-FG:500VDC>100MΩ	I/P-O/P: 500 VDC I/P-FG: 500 VDC O/P-FG: 500 VDC Ta:25°C	I/P-O/P: 9999MΩ I/P-FG: 9999MΩ O/P-FG: 9999MΩ NO DAMAGE
3	GROUNDING CONTINUITY	EN 60950-1 FG(PE) TO CHASSIS OR TRACE < 100 MΩ	40A / 2min Ta:25°C	10mΩ

### E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RADIATION	<input checked="" type="checkbox"/> EN55032 <input type="checkbox"/> EN55011 <input type="checkbox"/> CLASS A <input checked="" type="checkbox"/> CLASS B	I/P:24VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> PASS <input type="checkbox"/> FAIL Test by certified Lab
2	CONDUCTION	<input checked="" type="checkbox"/> EN55032 <input type="checkbox"/> EN55011 <input type="checkbox"/> CLASS A <input checked="" type="checkbox"/> CLASS B	I/P:24VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> PASS <input type="checkbox"/> FAIL Test by certified Lab
3	E.S.D	EN61000-4-2 <input type="checkbox"/> Din rail Model : AIR: 8KV / Contact: 6KV	I/P:24VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> CRITERIA A <input type="checkbox"/> CRITERIA B
4	E.F.T	EN61000-4-4 <input type="checkbox"/> INDUSTRY INPUT: 2KV	I/P: 24VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> CRITERIA A <input type="checkbox"/> CRITERIA B
5	SURGE	IEC61000-4-5 <input type="checkbox"/> INDUSTRY L-N :1KV L,N-FG:2KV	I/P: 24VDC O/P:FULL LOAD Ta:25°C	<input checked="" type="checkbox"/> CRITERIA A <input type="checkbox"/> CRITERIA B
6	Test by certified Lab & Test Report Prepare			

■ RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																																
1	TEMPERATURE RISE TEST	MODEL : DDR-240B-24 1. ROOM AMBIENT BURN-IN : 2 HRS I/P : 24VDC O/P : FULL LOAD Ta= 24.2 °C 2. HIGH AMBIENT BURN-IN : 2 HRS I/P : 24VDC O/P : FULL LOAD Ta= 50.8 °C																																																																																		
				<table border="1"> <thead> <tr> <th>NO</th> <th>Position</th> <th>ROOM AMBIENT Ta= 24.2 °C</th> <th>HIGH AMBIENT Ta= 50.8 °C</th> </tr> </thead> <tbody> <tr><td>1</td><td>LF1</td><td>74.0°C</td><td>105.3°C</td></tr> <tr><td>2</td><td>Q3</td><td>63.0°C</td><td>92.2°C</td></tr> <tr><td>3</td><td>LF100</td><td>77.8°C</td><td>107.8°C</td></tr> <tr><td>4</td><td>LF2</td><td>71.7°C</td><td>103.8°C</td></tr> <tr><td>5</td><td>T1</td><td>72.3°C</td><td>102.7°C</td></tr> <tr><td>6</td><td>T2</td><td>75.9°C</td><td>106.8°C</td></tr> <tr><td>7</td><td>L100</td><td>77.6°C</td><td>108.8°C</td></tr> <tr><td>8</td><td>C3</td><td>66.5°C</td><td>98.8°C</td></tr> <tr><td>9</td><td>D100</td><td>90.0°C</td><td>120.0°C</td></tr> <tr><td>10</td><td>D105</td><td>83.2°C</td><td>116.2°C</td></tr> <tr><td>11</td><td>C103</td><td>75.4°C</td><td>106.7°C</td></tr> <tr><td>12</td><td>C104</td><td>72.9°C</td><td>103.8°C</td></tr> <tr><td>13</td><td>Q6</td><td>67.8°C</td><td>98.8°C</td></tr> <tr><td>14</td><td>Q12</td><td>71.1°C</td><td>102.1°C</td></tr> <tr><td>15</td><td>C21</td><td>68.0°C</td><td>99.0°C</td></tr> <tr><td>16</td><td>R43</td><td>61.9°C</td><td>90.8°C</td></tr> <tr><td>17</td><td>U1</td><td>72.7°C</td><td>102.8°C</td></tr> <tr><td>18</td><td>C109</td><td>68.2°C</td><td>97.9°C</td></tr> <tr><td>19</td><td>TSW1</td><td>79.8°C</td><td>108.0°C</td></tr> </tbody> </table>	NO	Position	ROOM AMBIENT Ta= 24.2 °C	HIGH AMBIENT Ta= 50.8 °C	1	LF1	74.0°C	105.3°C	2	Q3	63.0°C	92.2°C	3	LF100	77.8°C	107.8°C	4	LF2	71.7°C	103.8°C	5	T1	72.3°C	102.7°C	6	T2	75.9°C	106.8°C	7	L100	77.6°C	108.8°C	8	C3	66.5°C	98.8°C	9	D100	90.0°C	120.0°C	10	D105	83.2°C	116.2°C	11	C103	75.4°C	106.7°C	12	C104	72.9°C	103.8°C	13	Q6	67.8°C	98.8°C	14	Q12	71.1°C	102.1°C	15	C21	68.0°C	99.0°C	16	R43	61.9°C	90.8°C	17	U1	72.7°C	102.8°C	18	C109	68.2°C	97.9°C	19	TSW1	79.8°C	108.0°C
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14	Q12	71.1°C	102.1°C																																																																																	
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19	TSW1	79.8°C	108.0°C																																																																																	
2	OVER LOAD BURN-IN TEST	NO DAMAGE 1 HOUR ( MIN )	I/P : 24 VDC O/P : 125 % LOAD Ta : 25°C	TEST : OK																																																																																
3	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 21.6 VDC/ 33.6 VDC O/P : 100 % LOAD Ta= -43 °C	TEST : OK																																																																																
4	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 55 °C NO DAMAGE	I/P : 36.6 VDC O/P : FULL LOAD Ta= 50 °C HUMIDITY= 95 %R.H	TEST : OK																																																																																
5	TEMPERATURE COEFFICIENT	± 0.03 %(0-55°C)	I/P : 24 VDC O/P : FULL LOAD	±0.0079%%(0-50°C)																																																																																
6	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : STATIC		TEST : OK																																																																																



7	THERMAL SHOCK TEST	1. Thermal shock Temperature : -45°C ~ +55°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 16 CYCLE 5. Input/Output condition : 24VDC/Full Load DC ON/OFF TEST turn on 3sec ; turn off 1sec@15cycle\ 24VDC/Full Load DC ON@1cycle	TEST : OK												
8	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 10min/sweep cycle (4) Acceleration : 5G (5) Test Time : 60min in each axis (X.Y.Z) (6) Ta : 25°C  2 Din Rail <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;"></th> <th style="width: 25%;">Displacement</th> <th style="width: 25%;">Acceleration</th> </tr> </thead> <tbody> <tr> <td>2 (+3/-0) Hz up to 15Hz</td> <td>±2.5mm</td> <td>-----</td> </tr> <tr> <td>15Hz up to 50Hz</td> <td>-----</td> <td>2.3g</td> </tr> <tr> <td>Sweep rate</td> <td colspan="2">Max 1 Octave/minute</td> </tr> </tbody> </table>		Displacement	Acceleration	2 (+3/-0) Hz up to 15Hz	±2.5mm	-----	15Hz up to 50Hz	-----	2.3g	Sweep rate	Max 1 Octave/minute		TEST : TEST : OK
	Displacement	Acceleration													
2 (+3/-0) Hz up to 15Hz	±2.5mm	-----													
15Hz up to 50Hz	-----	2.3g													
Sweep rate	Max 1 Octave/minute														
9	CAPACITOR LIFE CYCLE	SUPPOSE C103 IS THE MOST CRITICAL COMPONENT (1) I/P : 24VDC O/P : FULL LOAD Ta= 25 °C LIFE TIME (2) I/P : 24VDC O/P : FULL LOAD Ta= 50 °C LIFE TIME (3) I/P : 24VDC O/P : 75% LOAD Ta= 50 °C LIFE TIME (4) I/P : 24VDC O/P : 50% LOAD Ta= 50 °C LIFE TIME	(1) 296685 HRS (2) 37833.8 HRS (3) 106068.1 HRS (4) 246886.5 HRS												
10	MTBF	Conducted by Parts Stress Analysis Prediction 484.9K hrs min. Telcordia SR-332 (Bellcore) ; 189.9K hrs min. MIL-HDBK-217F (25°C)													
12	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure (Expected Life): Above 30,000 hours @ TA 50°C													

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		WANGDZ

2018.4.30 GP-A50-F010