



Test Report: IDLV-65-48

65W PWM Output LED Driver

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

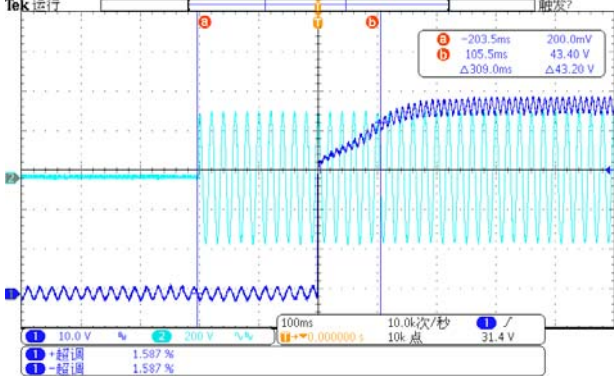
E.M.C. Test

■ RELIABILITY TEST

Environment Test

■ DESIGN VERIFY TEST

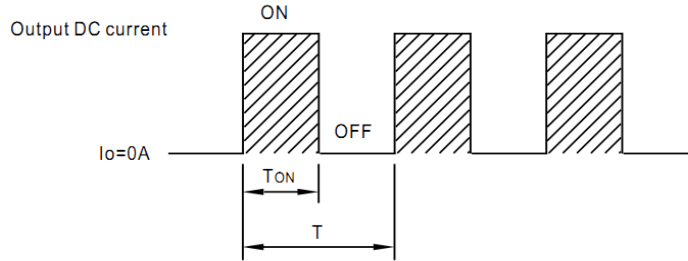
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM FREQUENCY	1KHz (±20%)	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	893Hz
2	VOLTAGE TOLERANCE	±10%	I/P: 180 VAC / 295 VAC O/P: FULL/ NO LOAD Ta: 25°C	-0.996% ~ 0.999%
3	OVER/UNDERSHOOT TEST	<±10 %	I/P: 230VAC O/P: FULL LOAD Ta: 25°C	<±10%
4	SET UP TIME(Max)	500ms/230VAC	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	309ms/230VAC
<p>INPUT=230VAC/50HZ @ FULL LOAD CH1: Output Voltage CH2: AC Input Voltage</p> 				
5	AUXILIARY DC OUTPUT (For A-Type only)	Nominal 12V (deviation 11.4~12.6) @50mA	I/P: 230 VAC O/P: FULL LOAD	11.92V

6 DIMMING TEST

※ Dimming principle for PWM style output

Dimming is achieved by varying the duty cycle of the output current.

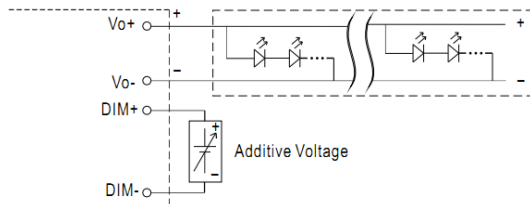


$$\text{Duty cycle(\%)} = \frac{T_{ON}}{T} \times 100\%$$

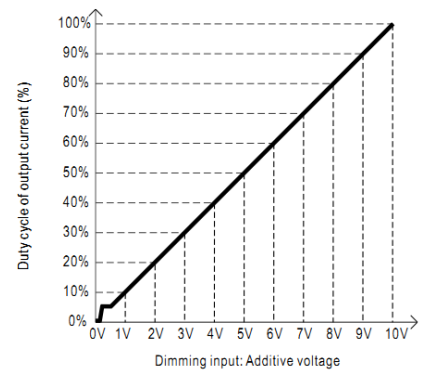
Output PWM frequency : 1KHz(±20%)

※ 2 in 1 dimming function

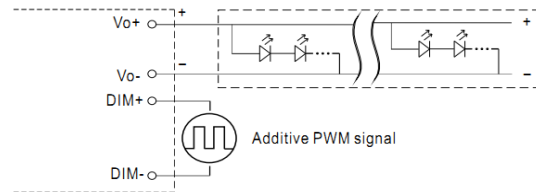
⊙ Applying additive 0 ~ 10VDC



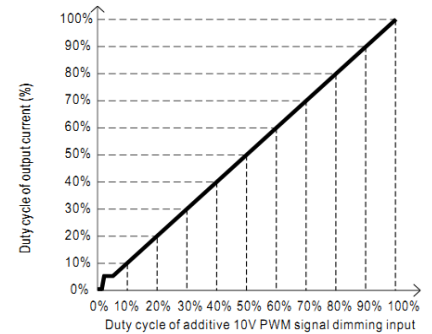
"DO NOT connect "DIM- to Vo-"



⊙ Applying additive 10V PWM signal (frequency range 300~3000Hz):



"DO NOT connect "DIM- to Vo-"



Note : 1. Min. duty cycle of output current is about 8% and the output current is not defined when 0% < Iout < 8%.

2. The duty cycle of output current could drop down to 0% when dimming input is about 0Vdc or 10V PWM signal with 0% duty cycle.

I/P: 230 VAC

O/P: DIMMING TEST

Ta: 25°C

1	Dimming voltage	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN
	Output Current	0	0.13A	0.27A	0.41A	0.55A	0.69A	0.83A	0.98A	1.11A	1.26A	1.40A	1.41A
Duty cycle of output current	0%	9.3%	19.3%	29.3%	39.3%	49.3%	59.3%	70.0%	79.3%	90.0%	100.0%	100.7%	
2	Dimming Duty cycle	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN
	Output Current	0	0.13A	0.27A	0.41A	0.55A	0.68A	0.83A	0.97A	1.11A	1.25A	1.39A	1.41A
	Duty cycle of output current	0%	9.3%	19.3%	29.3%	39.3%	48.6%	59.3%	69.3%	79.3%	89.3%	99.3%	100.7%

TEST RESULT: OK

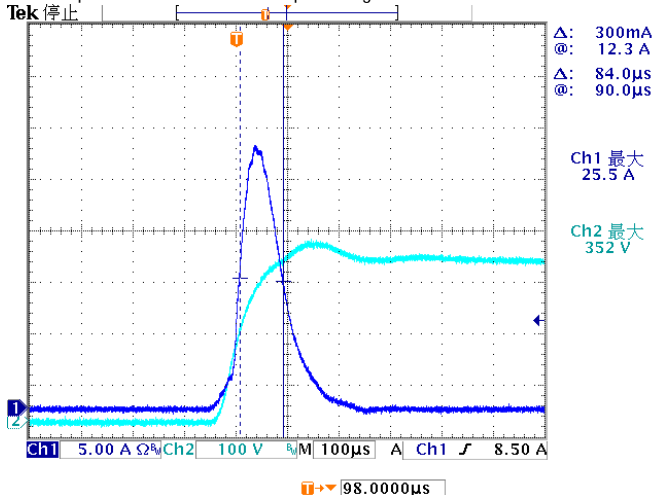


INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	180VAC~295VAC	I/P: TESTING O/P: FULL LOAD Ta: 25°C	177V~305V
			I/P: (1)LOW-LINE-3V=177 V HIGH-LINE+10V=305 V O/P: FULL/NO LOAD ON: 30 Sec OFF: 30 Sec 10MIN (2)230VAC ON: 0.5 Sec OFF: 0.5 Sec 20MIN (POWER ON/OFF NO DAMAGE)	TEST: OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P: 180 VAC ~295 VAC O/P: FULL~NO LOAD Ta: 25°C	TEST: OK
3	AC CURRENT	0.4A/230VAC 0.3A/277VAC	I/P: 230 VAC I/P: 277 VAC O/P: FULL LOAD Ta: 25°C	I = 0.315A/ 230VAC I = 0.270A/ 277VAC
4	LEAKAGE CURRENT	< 0.75mA / 277VAC	I/P: 277 VAC O/P: NO LOAD Ta: 25°C	L-FG: 0.0028 mA N-FG: 0.0029 mA
5	NO LOAD POWER CONSUMPTION	< 0.5W for Blank-Type < 1.2W for A-Type	I/P: 230VAC O/P: NO LOAD Ta: 25°C	0.457 W for Blank-Type 0.477 W for A-Type
6	INRUSH CURRENT(Typ)	COLD START 30A/230VAC Twidth =270 us measured at 50% Ipeak	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	I= 25.5A/ 230VAC Twidth =84 us

INPUT=230VAC/50HZ @ FULL LOAD

CH2: Input current CH1: AC Input Voltage



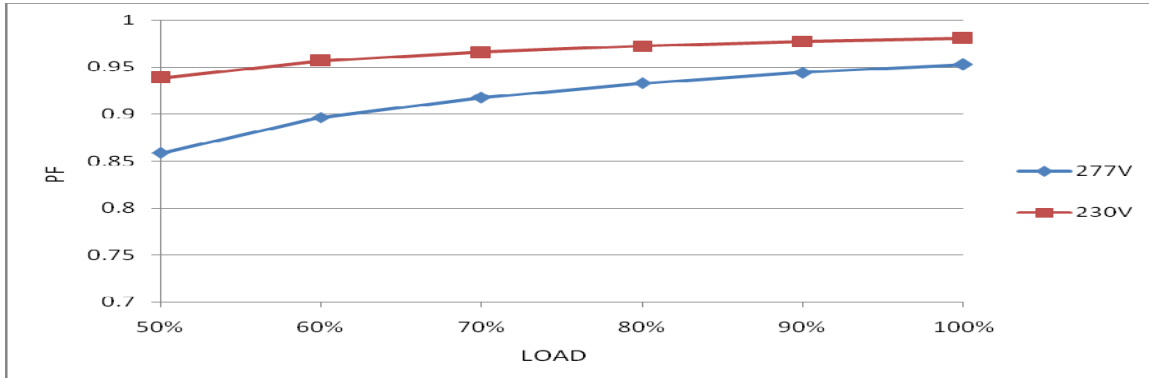


65W PWM Output LED Driver

IDLV-65 series

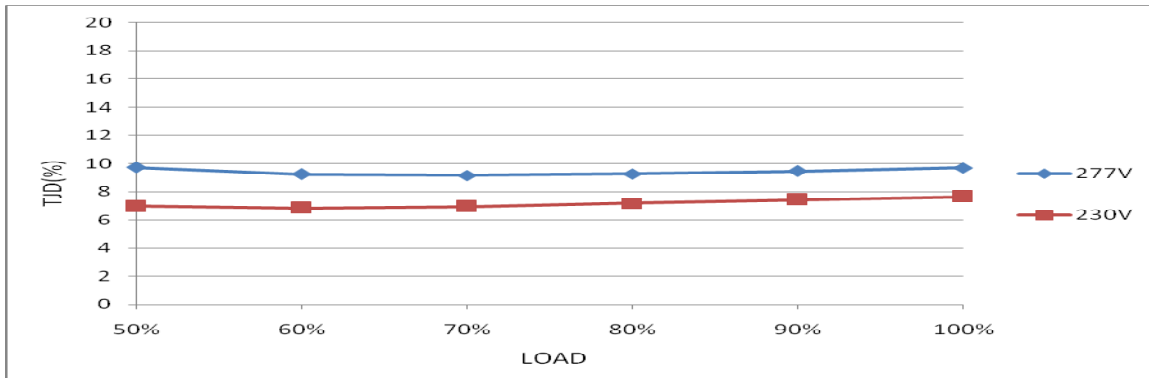
7	POWER FACTOR	0.95/ 230VAC 0.9/ 277VAC	I/P: 230 VAC I/P: 277 VAC O/P: FULL LOAD Ta: 25°C	PF=0.981 /230VAC PF=0.953 /277VAC
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PF vs LOAD



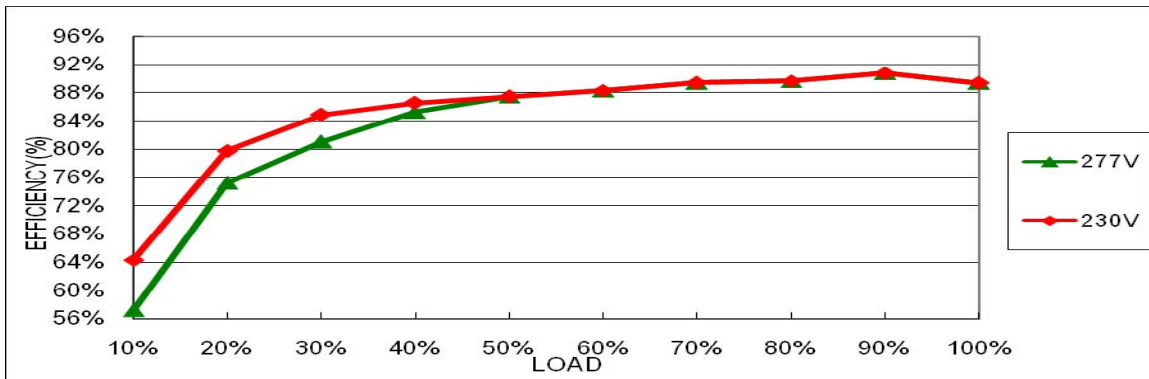
8	TOTAL HARMONIC DISTORTION	THD < 20% (@load ≥ 60%/230VAC; @load ≥ 75%/277VAC)	I/P: 230 VAC I/P: 277 VAC O/P: 60% / 75% LOAD Ta: 25°C	THD=6.83% @60% load /230VAC THD=9.17% @75% load /277VAC
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THD vs LOAD



9	EFFICIENCY(Typ)	89%	I/P: 230VAC O/P: FULL LOAD Ta: 25°C	89.38%
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EFFICIENCY vs LOAD





PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER CURRENT PROTECTION	105 %~ 115 %	I/P: 200VAC I/P: 230VAC I/P: 295VAC O/P: TESTING Ta: 25°C	108.7%/ 200VAC 108.8%/ 230VAC 108.9%/ 295VAC Hiccup mode, recovers automatically after fault condition is removed
2	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 180VAC I/P: 295VAC O/P: 80%/FULL LOAD Ta: 25°C	NO DAMAGE Shut down O/P voltage, re-power on to recovery

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Transistor (D to S) or (C to E) Peak Voltage	Q1 Rated 9A/800V	I/P: High-Line +3V =298V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25°C	(1) 732V (2) 728V (3) 698V
2	Diode Peak Voltage	D100 Rated 20A/300V	I/P: High-Line +3V =298V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25°C	(1) 283V (2) 281V (3) 279V
3	Control IC Voltage Test	U1 Rated 35V	I/P: High-Line +3V =298V O/P: (1) Full Load input on/off (2) NO load input on /Off (3) Full Load /NO load Change Ta: 25°C	(1) 15.0V (2) 12.7V (3) 15.0V



SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P: 3.75KVAC/min	I/P-O/P: 4.2KVAC/min Ta: 25°C	I/P-O/P: 1.640mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P: 500VDC>100MΩ	I/P-O/P: 500VDC Ta: 25°C	I/P-O/P: >9999MΩ

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P: 230VAC/50HZ O/P: FULL /60% LOAD Ta: 25°C	PASS
2	CONDUCTION	EN55015	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
3	RADIATION	EN55015	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY Air: 8KV Contact: 4KV	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 230VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
6	SURGE	EN61000-4-5 LIGHT INDUSTRY L-N: 1KV	I/P: 230VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
7	Test by certified Lab & Test Report Prepare			

■ **RELIABILITY TEST**

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																
1	TEMPERATURE RISE TEST	MODEL: IDLV-65-48 1. ROOM AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 28.4°C 2. HIGH AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 43.3°C																																																																		
		<table border="1"> <thead> <tr> <th>NO</th> <th>Position</th> <th>ROOM AMBIENT Ta= 28.4°C</th> <th>HIGH AMBIENT Ta=43.3°C</th> </tr> </thead> <tbody> <tr><td>1</td><td>BD1</td><td>82.3°C</td><td>97.5°C</td></tr> <tr><td>2</td><td>C8</td><td>77.0°C</td><td>93.0°C</td></tr> <tr><td>3</td><td>C6</td><td>77.1°C</td><td>93.9°C</td></tr> <tr><td>4</td><td>D1</td><td>95.8°C</td><td>113.0°C</td></tr> <tr><td>5</td><td>Q1</td><td>85.9°C</td><td>103.5°C</td></tr> <tr><td>6</td><td>U1</td><td>75.2°C</td><td>92.8°C</td></tr> <tr><td>7</td><td>R19</td><td>105.9°C</td><td>121.4°C</td></tr> <tr><td>8</td><td>T1</td><td>88.8°C</td><td>106.2°C</td></tr> <tr><td>9</td><td>D100</td><td>80.8°C</td><td>96.2°C</td></tr> <tr><td>10</td><td>Q100</td><td>62.7°C</td><td>78.7°C</td></tr> <tr><td>11</td><td>U100</td><td>63.7°C</td><td>80.8°C</td></tr> <tr><td>12</td><td>SCR1</td><td>65.1°C</td><td>92.7°C</td></tr> <tr><td>13</td><td>C105</td><td>63.3°C</td><td>78.6°C</td></tr> <tr><td>14</td><td>C107</td><td>52.1°C</td><td>67.4°C</td></tr> <tr><td>15</td><td>TC</td><td>65.7°C</td><td>82.6°C</td></tr> </tbody> </table>	NO	Position	ROOM AMBIENT Ta= 28.4°C	HIGH AMBIENT Ta=43.3°C	1	BD1	82.3°C	97.5°C	2	C8	77.0°C	93.0°C	3	C6	77.1°C	93.9°C	4	D1	95.8°C	113.0°C	5	Q1	85.9°C	103.5°C	6	U1	75.2°C	92.8°C	7	R19	105.9°C	121.4°C	8	T1	88.8°C	106.2°C	9	D100	80.8°C	96.2°C	10	Q100	62.7°C	78.7°C	11	U100	63.7°C	80.8°C	12	SCR1	65.1°C	92.7°C	13	C105	63.3°C	78.6°C	14	C107	52.1°C	67.4°C	15	TC	65.7°C	82.6°C		
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2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P: 295VAC/200VAC O/P: FULL LOAD Ta= -25°C	TEST: OK																																																																
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 40 °C NO DAMAGE	I/P: 305VAC O/P: FULL LOAD Ta=40 °C HUMIDITY= 95% R.H	TEST: OK																																																																
4	TEMPERATURE COEFFICIENT	±0.03%/°C (0-40°C)	I/P: 230 VAC O/P: FULL LOAD	±0.014%/°C (0-40°C)																																																																
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature: -45°C ~ +85°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 5 CYCLE 5. Input/Output condition: STATIC		TEST: OK																																																																
6	THERMAL SHOCK TEST	1. Thermal shock Temperature: -25°C ~ +45°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 10 CYCLE 5. Input/Output condition: 230VAC/Full Load AC ON/OFF TEST AC on 3 sec/AC off 1 sec TEST		TEST: OK																																																																



65W PWM Output LED Driver

IDLV-65 series

7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform: Sine Wave (2) Frequency: 10-500Hz (3) Sweep Time: 10min/sweep cycle (4) Acceleration: 2G (5) Test Time: 60min in each axis (X.Y.Z) (6) Ta: 25°C	TEST: OK
8	CAPACITOR LIFE CYCLE	IDLV-65-48: SUPPOSE C105 IS THE MOST CRITICAL COMPONENT (1) I/P: 230VAC O/P: FULL LOAD Ta= 25 °C LIFE TIME (2) I/P: 230VAC O/P: FULL LOAD Ta= 40 °C LIFE TIME (3) I/P: 230VAC O/P: 75% LOAD Ta= 40 °C LIFE TIME (4) I/P: 230VAC O/P: 50% LOAD Ta= 40 °C LIFE TIME	(1) 327804 HRS (2) 112817 HRS (3) 142788 HRS (4) 179975 HRS
9	MTBF	Conducted by Parts Stress Analysis Prediction 398.7K hrs min MIL-HDBK-217F (25°C)	
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure(Expected Life) : 30,000 hours @ Tcase 80°C; 50,000 hours @ Tcase 70°C	

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	CHENZH/ZHUOKB	SKY	LIUWY